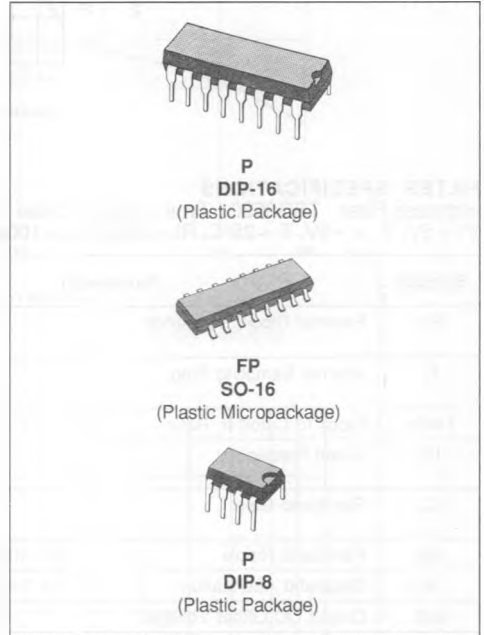


## SWITCHED CAPACITOR MASK PROGRAMMABLE FILTER

- CAUER TYPE
- 3TH ORDER
- STOPBAND ATTENUATION : 15dB (typ)
- PASSBAND RIPPLE : 0.2dB (typ)
- CLOCK TO CUT-OFF FREQ. RATIO : 320
- CLOCK FREQUENCY RANGE : 4 TO 2400kHz
- CUT-OFF FREQUENCY RANGE : 12Hz TO 7.5kHz

According to spectrum aliasing phenomenon, the TSG8530 must be considered as a highpass filter only in the range  $[F_c, F_i/2]$ , where  $F_i$  is the internal sampling frequency.

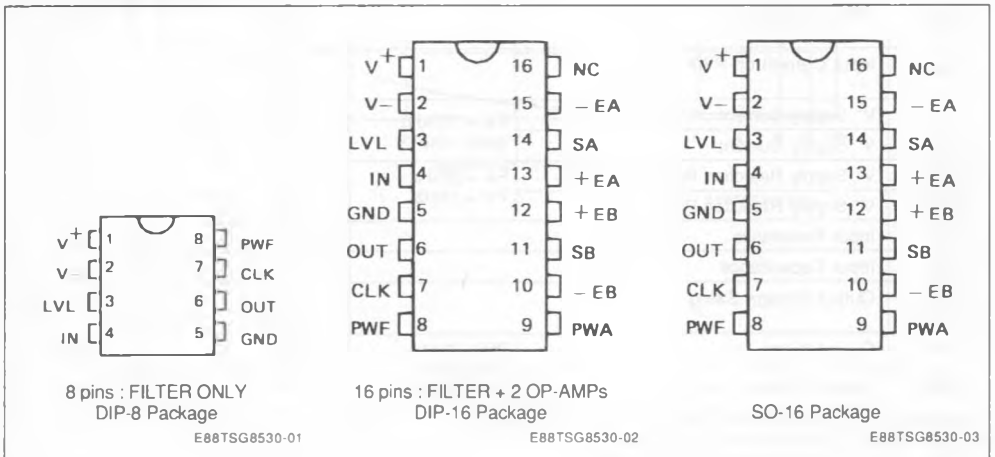
**Note :** For general characteristics, see TSG85XX specifications. For non standard quality level, consult SGS-THOMSON general ordering information.



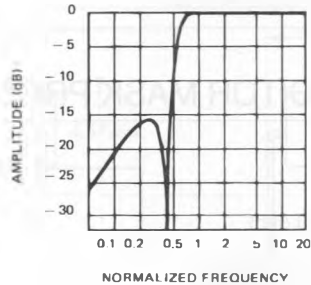
### DESCRIPTION

The TSG8530 is a HCMOS highpass\* elliptic filter.

### PIN CONNECTIONS



## AMPLITUDE RESPONSE CURVE



E88TSG8530-04

## FILTER SPECIFICATIONS

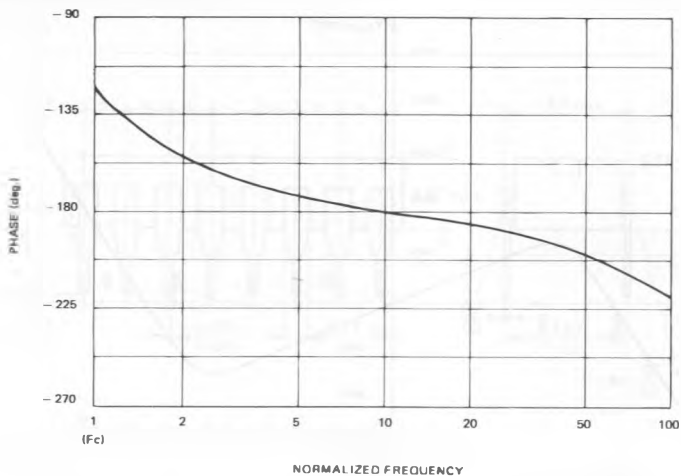
Highpass Filter : TSG8530 ; Type : Cauer ; Order : 3.

 $V^+ = 5V$ ,  $V^- = -5V$ ,  $T = 25^\circ C$ ,  $R_L = 5k\Omega$ ,  $C_L = 100pF$ ,  $I_{PWF} = 100\mu A$ 

Symbol	Parameter		Typ.	Tested Limits	Unit
Fe	External Clock Frequency		4 2400(*)		kHz (min) kHz (max)
Fi	Internal Sampling Freq.		2 1200(*)		kHz (min) kHz (max)
Fe/Fc	Clock to Cutoff fr. Ratio		320 $\pm$ 1%		
Fc	Cutoff Frequency		0.0125 7.5(*)		kHz (min) kHz (max)
G <sub>o</sub>	Passband Gain		- 0.3 0		dB (min) dB (max)
A <sub>p</sub>	Passband Ripple	[Fc, 30Fc] Fe = 320kHz	0.2	0.5	dB (max)
A <sub>s</sub>	Stopband Attenuation	F < 0.49Fc Fe = 320kHz	15	14	dB (min)
V <sub>off</sub>	Output DC Offset Voltage	LVL = 0V	$\pm$ 100	$\pm$ 200	mV (max)
LVL	DC Level Adjustment		$\pm$ 40		mV
LG	Level gain		- 6		
R <sub>PWF</sub>	PWF Resistance		10 72		k $\Omega$ (min) k $\Omega$ (max)
I <sub>PWF</sub>	Input Current on PWF		50 250		$\mu A$ (min) $\mu A$ (max)
I <sup>+</sup>	V <sup>+</sup> Supply Current	Fe = 100kHz I <sub>pwa</sub> = 0 $\mu A$	2.8	5	mA (max)
I <sup>-</sup>	V <sup>-</sup> Supply Current		2.8	5	mA (max)
PSRR <sup>+</sup>	V <sup>+</sup> Supply Rejection Ratio	Fe = 32kHz Fin = 1kHz	33		dB
PSRR <sup>-</sup>	V <sup>-</sup> Supply Rejection Ratio		38		dB
R <sub>IN</sub>	Input Resistance		3		M $\Omega$
C <sub>IN</sub>	Input Capacitance		20		pF
V <sub>o</sub>	Output Voltage Swing		+ 3.5 - 4.5		Vp-p (max)
V <sub>n</sub>	Output Noise	BW = 2kHz Fe = 32kHz Vin = 2Vrms	80		$\mu V$ rms
SNR	Signal to Noise Ratio		85		dB

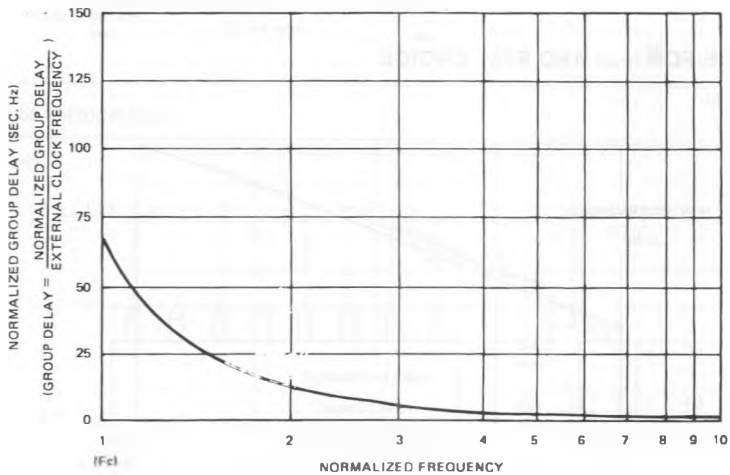
(\*) At maximum Fe : - stopband attenuation A<sub>s</sub> > 14dB for F < 0.49Fc  
(with I<sub>pwf</sub> = 250 $\mu A$ ) - passband ripple : A<sub>p</sub> = 0.2dB  
- passband gain : G<sub>o</sub> = - 0.6dB

PHASE RESPONSE CURVE (in passband)



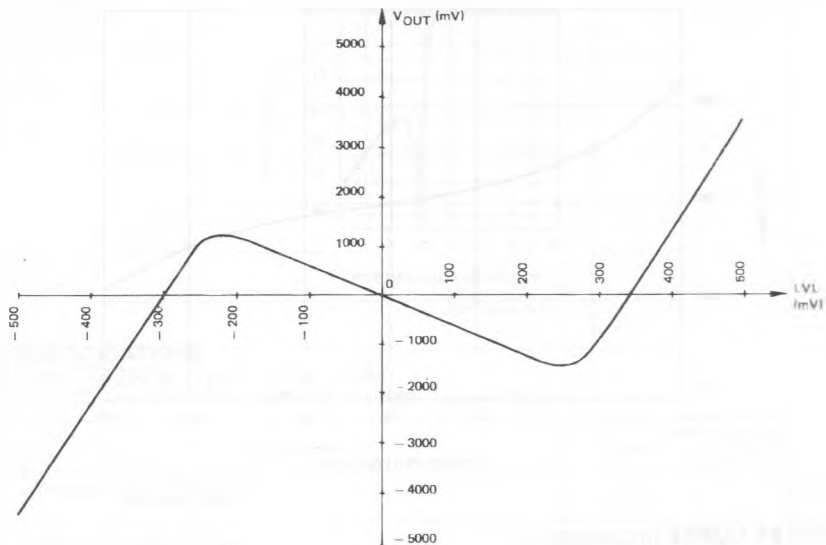
E88TSG8530-05

GROUP DELAY CURVE (in passband)



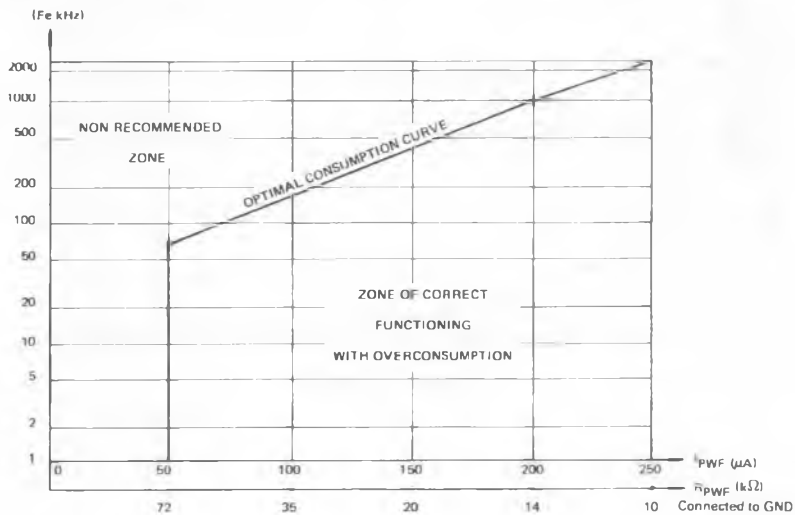
E88TSG8530-06

OUTPUT DC VOLTAGE ADJUSTMENT FROM LVL PIN



E88TSG8530-07

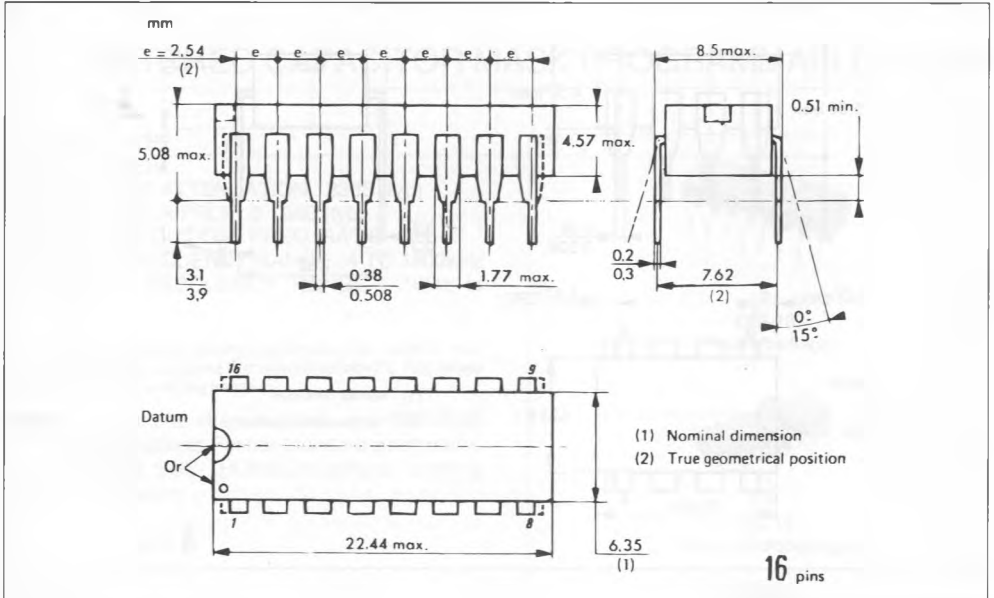
USER'S GUIDE FOR  $I_{PWF}$  AND  $R_{PWF}$  CHOICE



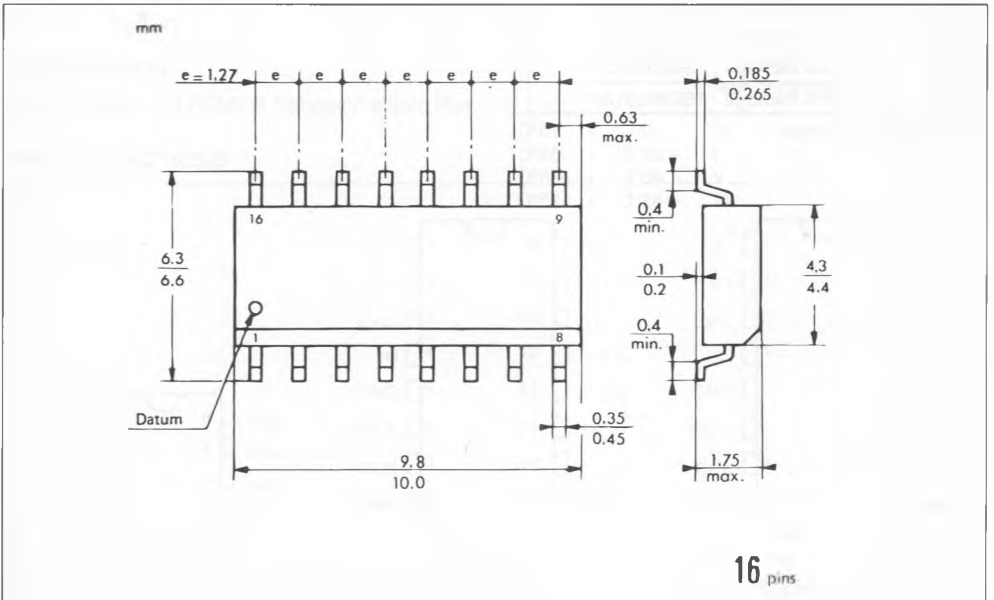
E88TSG8530-08

PACKAGE MECHANICAL DATA

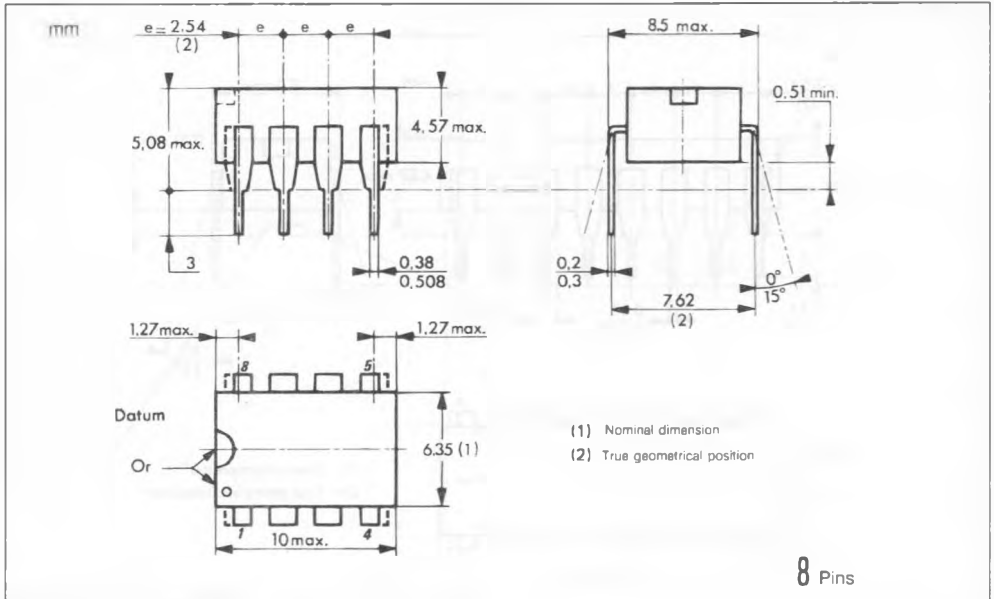
16 PINS - Plastic Dip



16 PINS - Plastic Micropackage



8 PINS - Plastic Dip



ORDER CODES

Plastic	16 Pins Package : TSG8530XP
Ceramic	16 Pins Package : TSG8530XC
Cerdip	16 Pins Package : TSG8530XJ
Plastic	8 Pins Package : TSG85301XP

X : Temperature Range = C : 0°C + 70°C  
 I : - 25°C + 85°C  
 V : - 40°C + 85°C  
 M : - 55°C + 125°C