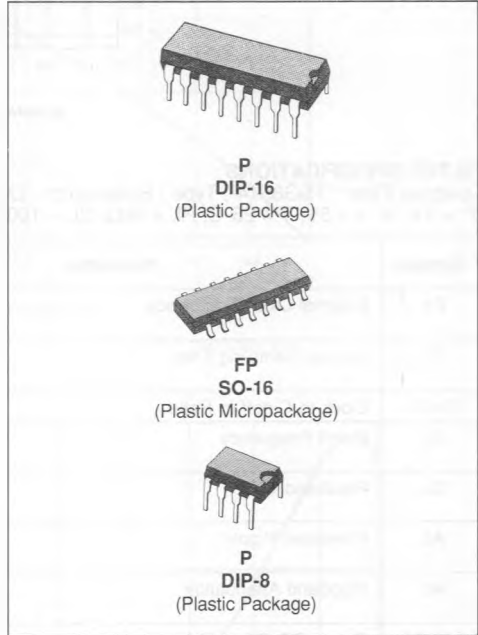


SWITCHED CAPACITOR MASK PROGRAMMABLE FILTER

- BUTTERWORTH TYPE
- 8TH ORDER
- STOPBAND ATTENUATION : 74dB (typ) AT $3.6 \times F_c$
- PASSBAND RIPPLE : MAXIMALLY FLAT
- CLOCK TO CUT-OFF FREQ. RATIO : 80
- CLOCK FREQUENCY RANGE : 1 TO 1000kHz
- CUT-OFF FREQUENCY RANGE : 12.5Hz TO 12.5kHz

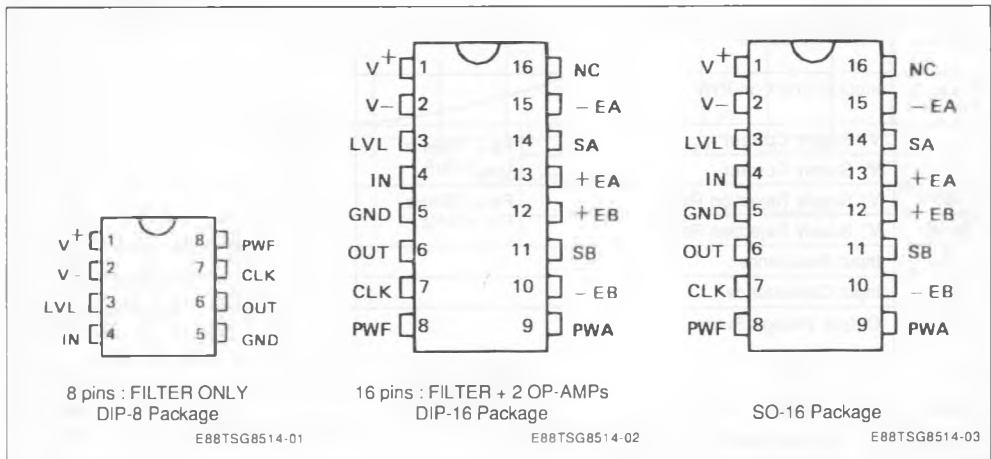
Note : For general characteristics, see TSG85XX specifications. For non standard quality level, consult SGS-THOMSON general ordering information.



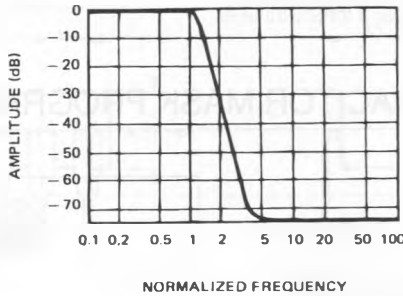
DESCRIPTION

The TSG8514 is a HCMOS lowpass polynomial filter.

PIN CONNECTIONS



AMPLITUDE RESPONSE CURVE



E88TSG8514-04

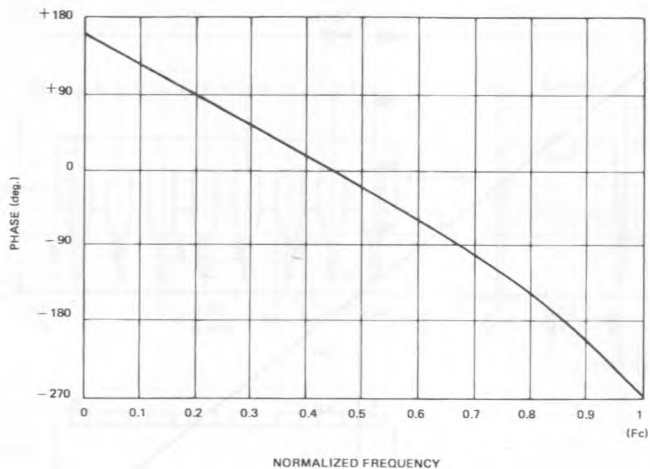
FILTER SPECIFICATIONS

Lowpass Filter : TSG8514 ; Type : Butterworth ; Order : 8.
 $V^+ = 5V$, $V^- = -5V$, $T = 25^\circ C$, $R_L = 5k\Omega$, $C_L = 100pF$, $I_{PWF} = 100\mu A$

Symbol	Parameter		Typ.	Tested Limits	Unit
Fe	External Clock Frequency		1 1000(*)		kHz (min) kHz (max)
Fi	Internal Sampling Freq.		0.5 500(*)		kHz (min) kHz (max)
Fe/Fc	Clock to Cutoff fr. Ratio		80 ± 1%		
Fc	Cutoff Frequency		0.0125 12.5(*)		kHz (min) kHz (max)
Go	Passband Gain		- 0.3 0		dB (min) dB (max)
Ap	Passband Ripple	Fe = 80kHz	maxi mally Flat		dB (max)
As	Stopband Attenuation	Fe = 80kHz F > 3.6 Fc	74	68	dB (min)
Voff	Output DC Offset Voltage	LVL = 0V	± 100	± 200	mV (max)
LVL	DC Level Adjustment		± 100		mV
LG	Level gain		- 2		
RPWF	PWF Resistance		10 72		kΩ (min) kΩ (max)
IPWF	Input Current on PWF		50 250		μA (min) μA (max)
I*	V* Supply Current	Fe = 100kHz I _{pwa} = 0μA	3.8	5	mA (max)
I ⁻	V ⁻ Supply Current		3.8	5	mA (max)
PSRR*	V* Supply Rejection Ratio	Fe = 160kHz Fin = 1kHz	30		dB
PSRR ⁻	V ⁻ Supply Rejection Ratio		42		dB
RIN	Input Resistance		3		MΩ
CIN	Input Capacitance		20		pF
Vo	Output Voltage Swing		+ 3.5 - 4.5		Vp-p (max)
Vn	Output Noise	BW = 3.4kHz Fe = 256kHz	86		μVrms
SNR	Signal to Noise Ratio	Vin = 2Vrms	87		dB

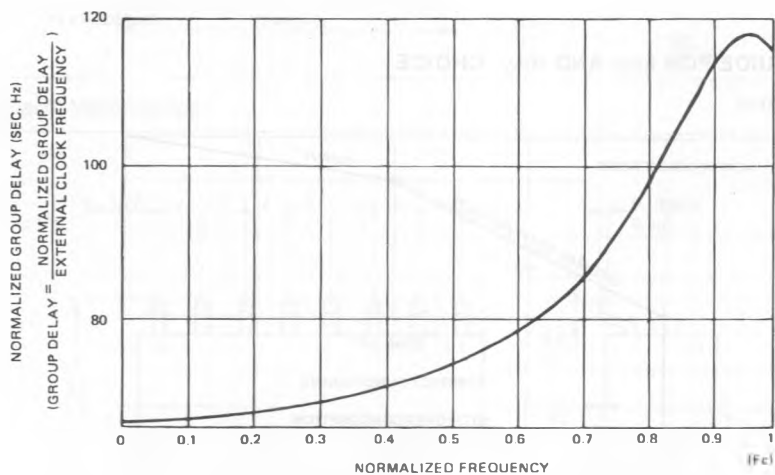
(*) At maximum Fe : - stopband attenuation As > 50dB for F > 3.6Fc
 (with I_{pwi} = 250μA) - passband gain : Go = - 0.5dB

PHASE RESPONSE CURVE (in passband)



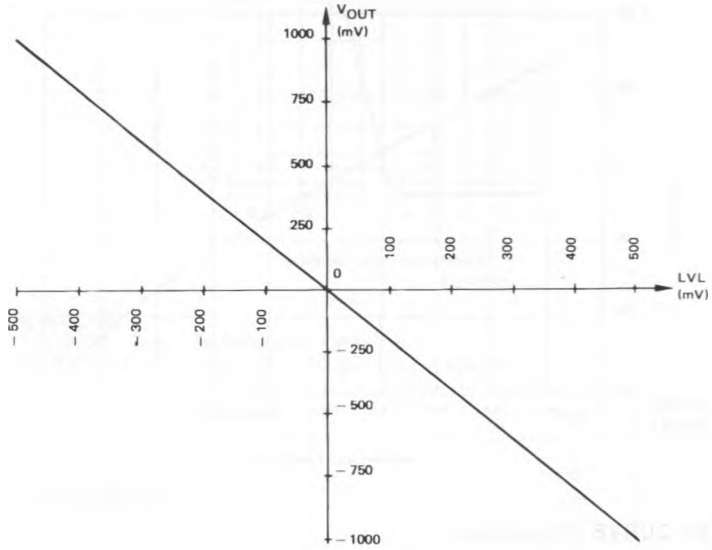
E88TSG8514-05

GROUP DELAY CURVE (in passband)



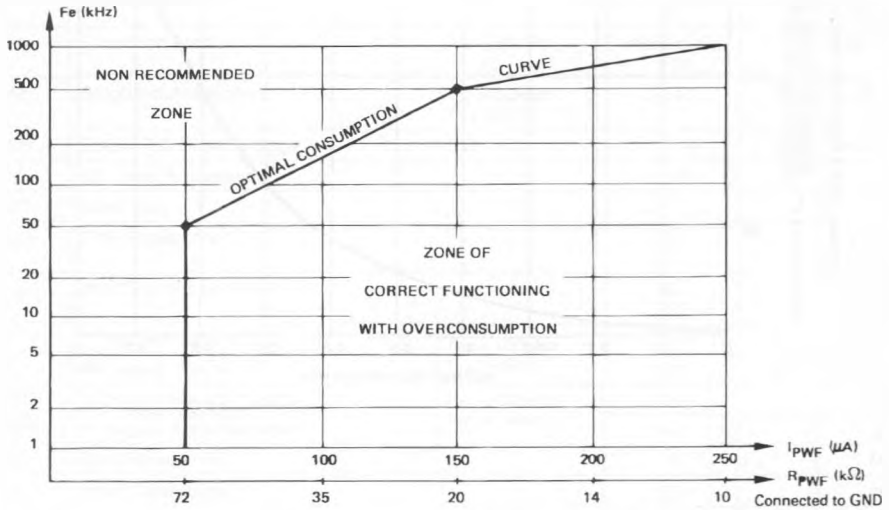
E88TSG8514-06

OUTPUT DC VOLTAGE ADJUSTMENT FROM LVL PIN



E88TSG8514-07

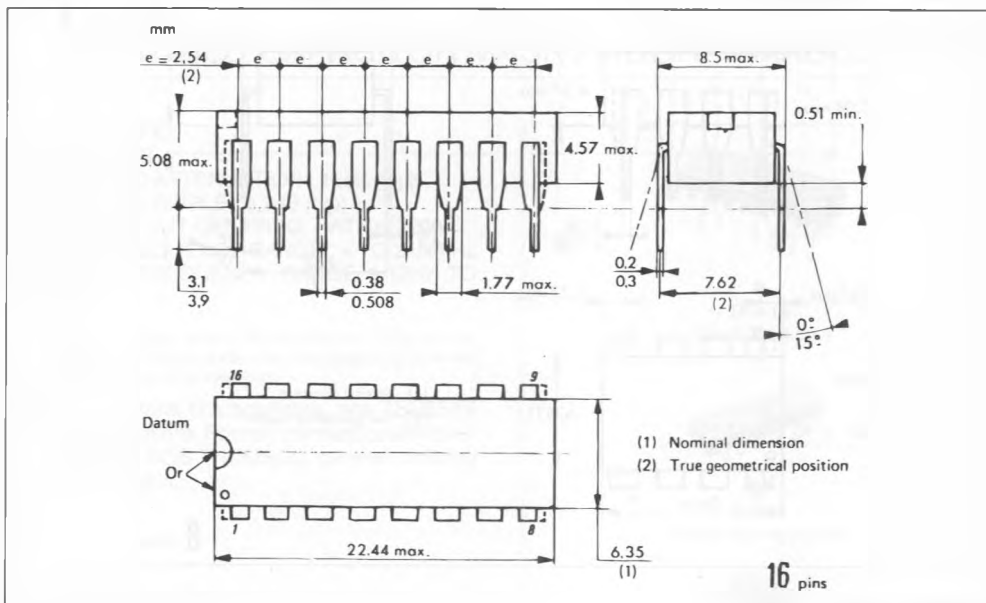
USER'S GUIDE FOR I_{PWF} AND R_{PWF} CHOICE



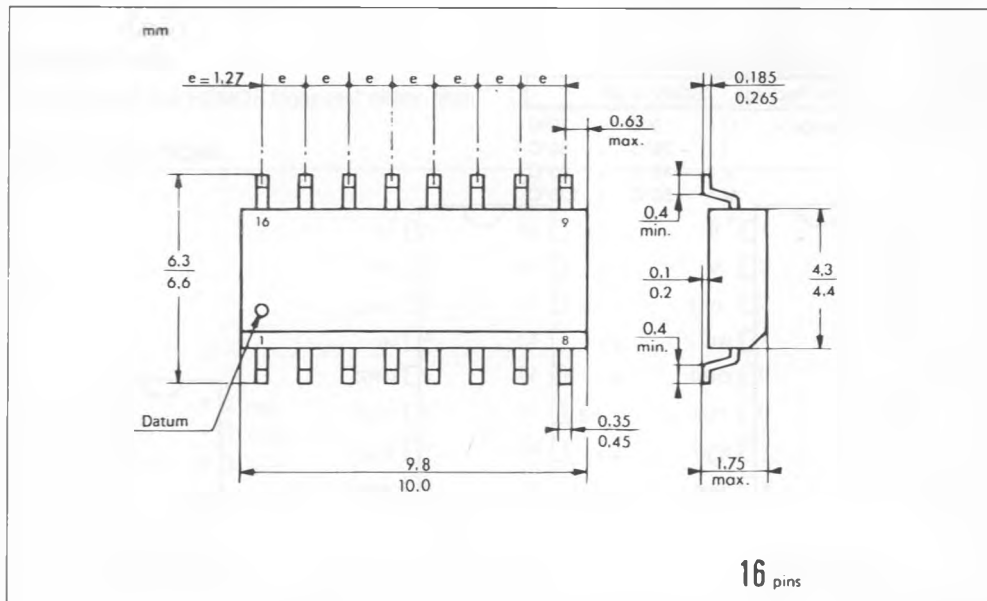
E88TSG8514-08

PACKAGE MECHANICAL DATA

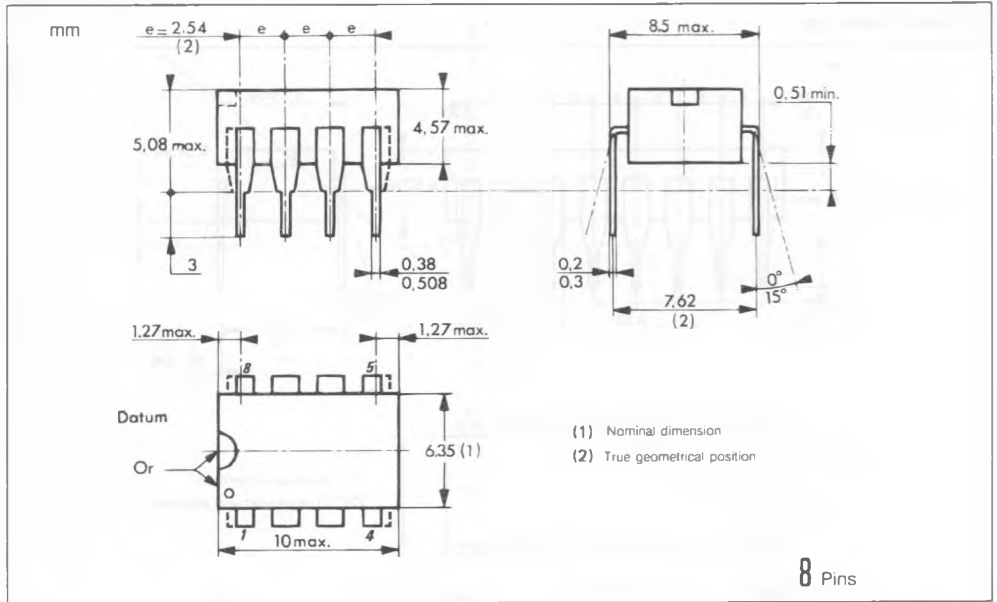
16 PINS - Plastic Dip



16 PINS - Plastic Micropackage



8 PINS - Plastic Dip



ORDER CODES

Plastic	16 Pins Package : TSG8514XP
Ceramic	16 Pins Package : TSG8514XC
Cerdip	16 Pins Package : TSG8514XJ
Plastic	8 Pins Package : TSG85141XP

X : Temperature Range = C : 0°C + 70°C
 I : - 25°C + 85°C
 V : - 40°C + 85°C
 M : - 55°C + 125°C