TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WBD126FK

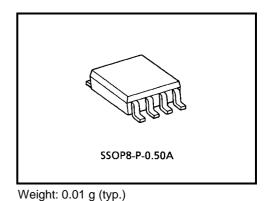
#### Dual Bus Switch with Level Shift

The TC7WBD126FK is a low on-resistance, high-speed CMOS 2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

The internal diode which adds to power supply line is enable to realize the shift of signal level from 5 V to 3.3 V. (Note 1)

All inputs are equipped with protector circuits to protect the device from static discharge.



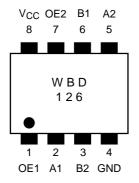
#### Features

- Operating voltage:  $V_{CC} = 4.5 \sim 5.5 V$
- High speed operation:  $t_{pd} = 0.25 \text{ ns} (max)$
- Ultra-low on resistance:  $R_{ON} = 5 \Omega$  (typ.)
- Electro-static discharge (ESD) performance: ±200 V or more (JEITA)

±2000 V or more (MIL)

- TTL level input (control input)
- Package: US8
  - Note 1: In case that over-shoot noise is detected, this device should be used with clamp diode to prevent the next stage device from over-stress.

#### Pin Assignment (top view)

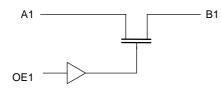


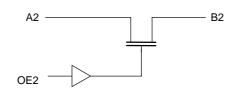
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#### **Truth Table**

Inputs	Function		
OE	Function		
L	Disconnect		
Н	A port = B port		

#### System Diagram





### **Maximum Ratings**

Characteristics	Symbol	Rating	Unit
Power supply range	V <sub>CC</sub>	-0.5~7.0	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V
DC switch voltage	VS	-0.5~7.0	V
Input diode current	I <sub>IK</sub>	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	PD	200	mW
DC V <sub>CC</sub> /GND current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65~150	°C

## **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Switch voltage	VS	0~5.5	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

#### **Electrical Characteristics**

#### DC Characteristics (Ta = -40~85°C)

Character	istics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Typ. (Note 2)	Max	Unit
Input voltage	"H" level	VIH	_		4.5~5.5	2.0	—	_	V
input voltage	"L" level	VIL	_		4.5~5.5	_	—	0.8	v
High-level output	voltage	V <sub>OH</sub>	Figure 4			_			
Input leakage cur	rent	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5 V		4.5~5.5	_	—	±1.0	μA
Power off leakage	e current	I <sub>OFF</sub>	A, B, OE = 0~5.5 V		0	_	—	±1.0	μA
Off-state leakage (switch off)	current	I <sub>SZ</sub>	A, B = 0~5.5 V, OE = GND		4.5~5.5		_	±1.0	μA
	N resistance (Note 3)		V <sub>IS</sub> = 0 V	$I_{IS} = 30 \text{ mA}$	4.5	_	5	7	
ON resistance			VIS = 0 V	I <sub>IS</sub> = 64 mA	4.5	_	5	7	Ω
(Note 3)			$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5	_	35	50	
Quiescent supply current		laa	V <sub>IN</sub> = V <sub>CC</sub> or GND	Switch ON	5.5			1.5	mA
		Icc	$I_{OUT} = 0$	Switch OFF	5.5			10	μA
		$\Delta I_{CC}$	V <sub>IN</sub> = 3.4 V (one input)	(Note 4)	5.5			2.5	mA

Note 2: Typical values are at  $V_{CC} = 5 V$  and  $Ta = 25^{\circ}C$ .

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

Note 4: Quiescent supply current at V<sub>CC</sub> = 3.4 V will be sum of I<sub>CC</sub> and  $\Delta$ I<sub>CC</sub>.

#### AC Characteristics (Ta = -40~85°C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t <sub>pLH</sub>	Figure 1, Figure 2 (Note 5)	4.5	_	0.25	ns
	t <sub>pHL</sub>					
Output enable time	t <sub>pZL</sub> t <sub>pZH</sub>	Figure 1, Figure 3	4.5		4.5	ns
Output disable time	t <sub>pLZ</sub> t <sub>pHZ</sub>	Figure 1, Figure 3	4.5	_	5.5	ns

Note 5: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

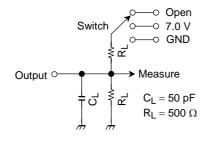
#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Control pin input capacitance	C <sub>IN</sub>	(Not	e 6) 5.0	3	pF
Switch terminal capacitance	C <sub>I/O</sub>	OE = GND (Not	e 6) 5.0	10	pF

Note 6: This parameter is guaranteed by design.

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#### **AC Test Circuit**



Parameter	Switch
t <sub>pLH</sub> , t <sub>pHL</sub>	Open
t <sub>pLZ</sub> , t <sub>pZL</sub>	7.0 V
t <sub>pHZ</sub> , t <sub>pZH</sub>	Open

Figure 1

#### **AC Waveform**

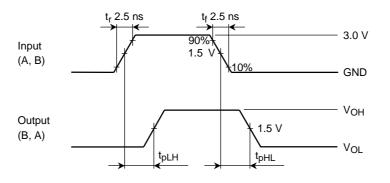
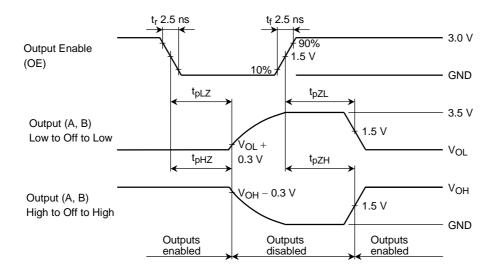
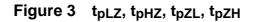
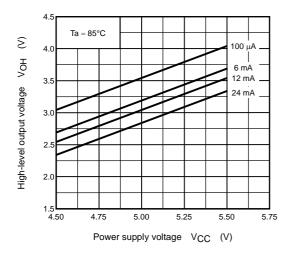


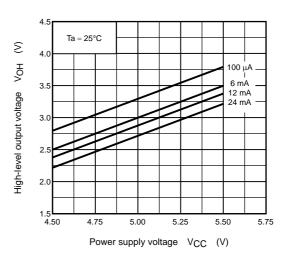
Figure 2  $t_{pLH}, t_{pHL}$ 





### V<sub>OH</sub> – V<sub>CC</sub> Characteristics (typ.)





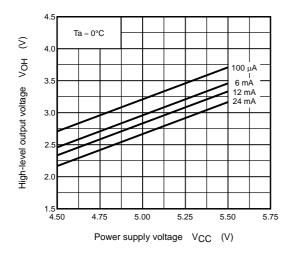


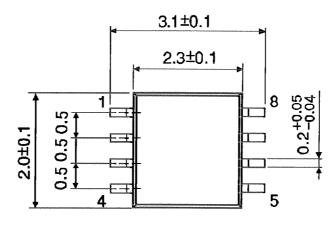
Figure 4

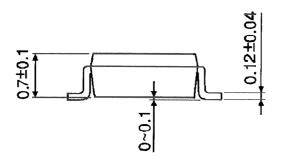
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### Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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