TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SBD385FU

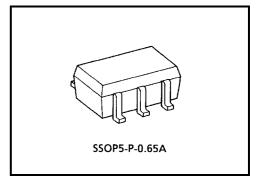
Single Bus Switch with Level Shifting

The TC7SBD385FU provides single bit of high-speed TTL-compatible switching. The low on resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as just 1-bit low-impedance switch with output-enable (OE) input. When OE is high, the switch is on and data can flow from port A to port B, or vice versa. When OE is low, the switch is open and a high-impedance state exists between the two ports.

The internal diode which adds to power supply line is enable to realize the shift of signal level from 5 V to 3.3 V. (Note 1)

All inputs are equipped with protection circuits against static discharge.

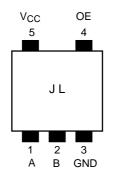


Weight: 0.006 g (typ.)

Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.25 \text{ ns} (\text{max})$
- Low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- ESD performance: Machine model > ± 200 V Human body model > ± 2000 V
- TTL level input (control input)
- Package: USV
 - Note 1: In case that over-shoot noise is detected, this device should be used with clamp diode to prevent the next stage device from over-stress.

Pin Assignment (top view)

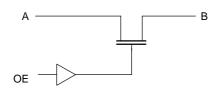


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Truth Table

Input	Function			
OE	Function			
L	Disconnect			
Н	A port = B port			

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	VS	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	PD	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	VS	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Character	istics	Symbol	Test Condition V		V _{CC} (V)	Min	Typ. (Note 2)	Max	Unit
	"H" level	VIH	_		4.5~5.5	2.0	_	_	V
Input voltage	"L" level	VIL			4.5~5.5	_	_	0.8	v
High-level output	voltage	V _{OH}	Figure 4		_	_	_	_	
Input leakage cur	rent	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_		±1.0	μA
Power off leakage	e current	I _{OFF}	A, B, OE = 0~5.5 V		0		_	±1.0	μA
Off-state leakage (switch off)	current	I _{SZ}	A, B = 0~5.5 V, OE = GND		4.5~5.5		_	±1.0	μΑ
	ON resistance			$I_{IS} = 30 \text{ mA}$	4.5	_	5	7	Ω
ON resistance			$V_{IS} = 0 V$	I _{IS} = 64 mA	4.5	_	5	7	
(Note 3)			$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5		35	50	
		Icc	$V_{IN} = V_{CC} \text{ or GND},$ $I_{OUT} = 0$	Switch ON	5.5			1.5	mA
Quiescent supply	Quiescent supply current			Switch OFF	5.5			10	μA
		ΔI_{CC}	VIN = 3.4 V (one input) (Note 4)		5.5			2.5	mA

Note 2: Typical values are at $V_{CC} = 5 V$ and $Ta = 25^{\circ}C$.

- Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.
- Note 4: Quiescent supply current at V_{CC} = 3.4 V will be sum of I_{CC} and Δ I_{CC}.

AC Characteristics ($Ta = -40 \sim 85^{\circ}C$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2 (Note 5)	4.5		0.25	ns
(bus to bus)	t _{pHL}		4.5		0.25	115
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5	_	4.5	ns
	t _{pZH}		4.0		ч.0	113
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5		5.0	ns
	t _{pHZ}		4.5		5.0	115

Note 5: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical on resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage the source (zero output impedance).

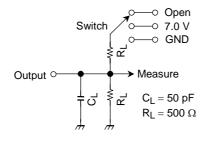
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}		(Note 6)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = GND	(Note 6)	5.0	10	pF

Note 6: This item is guaranteed by design.

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AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	7.0 V
t _{pHZ} , t _{pZH}	Open



AC Waveform

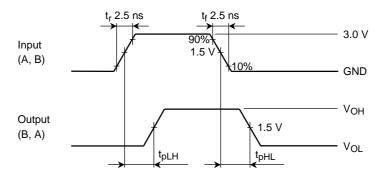


Figure 2 t_{pLH}, t_{pHL}

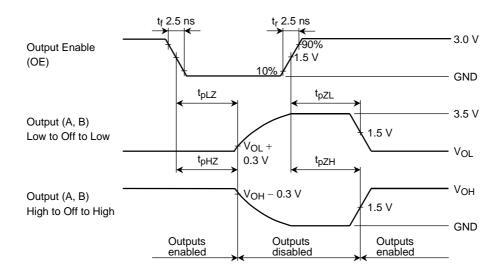
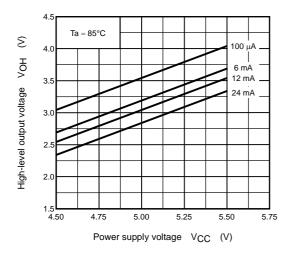
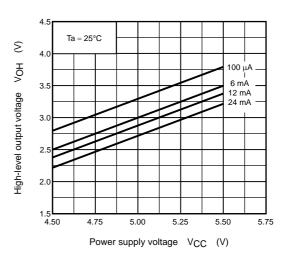


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

V_{OH} – V_{CC} Characteristics (typ.)





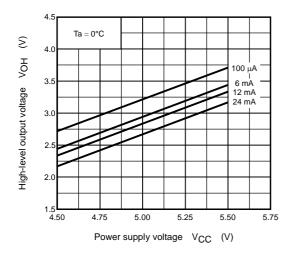
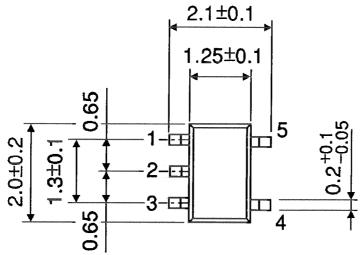


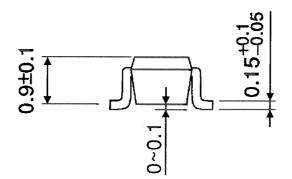
Figure 4

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Package Dimensions

Unit : mm





Weight: 0.006 g (typ.)

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