#### TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

## **TA8231L**

#### 42W BTL × 2CH Audio Power IC

The TA8231L is BTL audio power amplifier for consumer application. It is designed for high power,low distortion and low noise.

It contains various kind of protectors and the function of stand-by.

#### **Feature**

- High power
  - : POUT(1) = 42W (typ.)

$$(\mathrm{V_{CC}}=14.4\mathrm{V},\,\mathrm{f}=1\mathrm{kHz},\,\mathrm{THD}=10\%,\,\mathrm{R_{L}}=2\Omega)$$

: POUT(2) = 37W (typ.)

$$(V_{CC} = 13.2V, f = 1kHz, THD = 10\%, RL = 2\Omega)$$

: POUT(3) = 22W (typ.)

$$(V_{CC} = 13.2V, f = 1kHz, THD = 10\%, R_L = 4\Omega)$$

- Excellent output power bandwidth
  - : POUT(4) = 17W(typ.)

$$(V_{CC} = 13.2V, f = 20Hz\sim20kHz, THD = 1\%, R_L = 4\Omega)$$

- · Fixed voltage gain
  - $: G_V = 32dB (typ.)$

$$V_{CC} = 13.2V, V_{out} = 0.775V_{rms} (0dBm)$$

• Low thermal resistance

$$\theta_{j-c} = 1.5$$
°C / W (typ.)

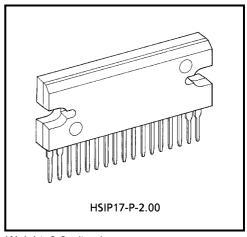
- Low distortion
  - : THD = 0.02% (typ.)

$$(V_{CC} = 13.2V, f = 1kHz, P_{out} = 4W, R_L = 4\Omega)$$

- Built-in stand-by switch function
  - $: ISTBY = 100\mu A (typ.)$

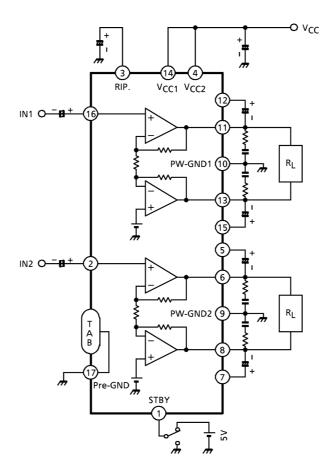
(With pin(1) set at high, power is turned on)

- Built-in various protection circuit
  - : Thermal shut down, Over voltage, Out to GND, Out to VCC, Out to Out short
- Operating supply voltage
  - $V_{CC} (opr) = 9 \sim 18V$



Weight: 9.8g (typ.)

## Block Diagram (G<sub>V</sub> = 32dB)



## **Caution And Application Method**

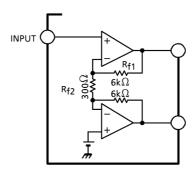
(Description is made only on the single channel.)

1. Voltage gain adjustment

This IC has no NF (negative feedback) terminals. Therefore, the voltage gain can't adjusted, but it makes the device a space and total costs saver.

The voltage gain is decided only internal by expression below.

$$G_V = 20 \lambda og \frac{R_{f1}}{R_{f2}} + 6 \text{ (dB)} = 32 \text{ (dB)}$$



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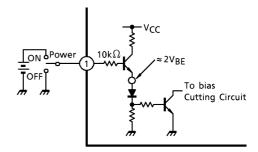
#### 2. Stand-by SW function

By means of controlling pin(1) (stand-by terminal) to high and low,the power supply can be set to on and off.

The threshold voltage of pin(1) is set at about 3V (typ.), and the power supply current is about  $100\mu A$  (typ.) at the stand-by state.

Pin (1) control voltage: V (SB)

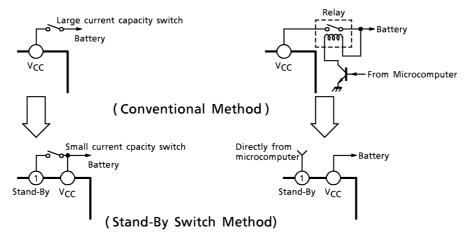
	8 (22)	
Stand-by	Power	V <sub>(SB)</sub> (V)
On	Off	0~2
Off	On	3~V <sub>CC</sub>



(Fig. 2) With pin① set to High, Power is turned ON.

#### ~Advantage of stand-by SW~

- (1) Since  $V_{CC}$  can directly be controlled to on / off by the microcomputer, the switching relay can be omitted.
- (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.



#### 3. Preventive measure against oscillation

For preventing the oscillation, it is advisable to use C4, the condenser of polyester film having small characteristic fluctuation of the temperature and the frequency.

The resistance R to be series applied to C4 is effective for phase correction of high frequency, and improves the oscillation allowance.

Since the oscillation allowance is varied according to the causes described below, perform the temperature test to check the oscillation allowance.

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- (1) Capacity value of condenser
- (2) Kind of condenser
- (3) Layout of printed board

## **Maximum Ratings (Ta = 25°C)**

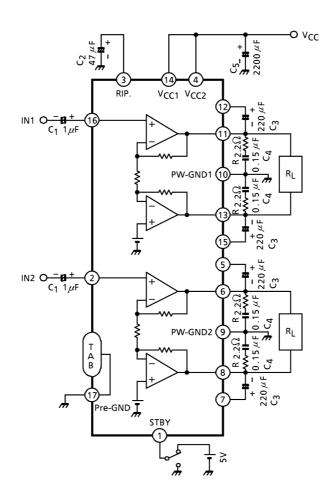
Characteristic	Symbol	Rating	Unit
Peak supply voltage (0.2s)	V <sub>CC</sub> (surge)	50	V
DC supply voltage	V <sub>CC (DC)</sub>	25	V
Operating supply voltage	V <sub>CC (opr)</sub>	18	V
Output current (peak)	I <sub>O (peak)</sub>	9	Α
Power dissipation	P <sub>D</sub>	50	W
Operating temperature	T <sub>opr</sub>	-30~85	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

# Electrical Characteristics (unless otherwise specified $V_{CC}$ = 13.2V, f = 1kHz, $R_g$ = 600 $\Omega$ , Ta = 25°C)

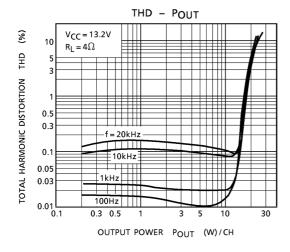
Characteristic	Symbol	Test Cir– cuit	Condition	Min	Тур.	Max	Unit
Quiescent current	I <sub>ccq</sub>	_	V <sub>in</sub> = 0	_	150	250	mA
Output power	Pout (1)	_	V <sub>CC</sub> = 14.4V, THD = 10%, R <sub>L</sub> = 2Ω	_	42	_	w
	Pout (2)	_	THD = 10%, R <sub>L</sub> = 2Ω	28	37	_	W
	Pout (3)	_	THD = 10%, R <sub>L</sub> = 4Ω	19	22	_	W
	Pout (4)	_	THD = 1%, f = 20Hz~20kHz, R <sub>L</sub> = $4\Omega$	_	17	_	W
Total harmonic distortion	THD	_	P <sub>out</sub> = 4W	_	0.02	0.2	%
Voltage gain	G <sub>V</sub>	_	V <sub>out</sub> = 0.775V <sub>rms</sub> (0dBm)	30.5	32	33.5	dB
Voltage gain ratio	$\Delta G_{V}$	_	V <sub>out</sub> = 0.775V <sub>rms</sub> (0dBm)	-1	0	1	dB
Output noise voltage	V <sub>NO (1)</sub>	_	$R_g = 0\Omega$ , DIN45405 Noise Filter	_	0.07	_	mV <sub>rms</sub>
	V <sub>NO (2)</sub>	_	$R_g = 0\Omega$ , BW = 20Hz~20kHz	_	0.06	0.1	mV <sub>rms</sub>
Ripple rejection ratio	R.R.	_	$f_{ripple} = 100 \text{Hz}, \\ V_{out} = 0.775 V_{rms} \text{ (0dBm)}, \\ R_g = 600 \Omega$	40	60	_	dB
Cross talk	C.T.	_	$R_g = 600\Omega, V_{out} = 0.775V_{rms} (0dBm)$	_	60	_	dB
Input resistance	R <sub>IN</sub>	_	_	_	30	_	kΩ
Output offset voltage	V <sub>offset</sub>	_	V <sub>in</sub> = 0	-200	0	200	mV
Stand-by current	I <sub>STBY</sub>	_	Pin(1): GND	_	100	150	μA

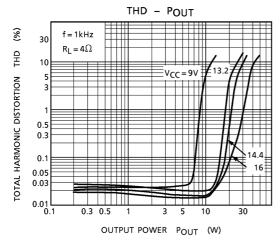
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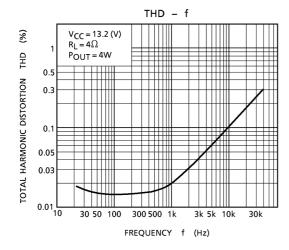
Test Circuit (G<sub>V</sub> = 32dB)

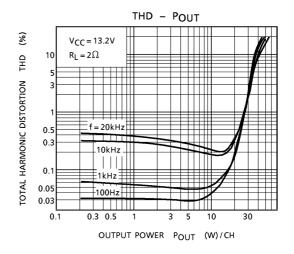


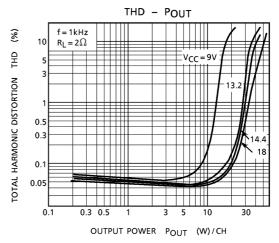
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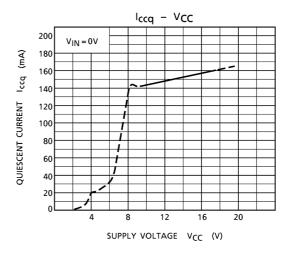


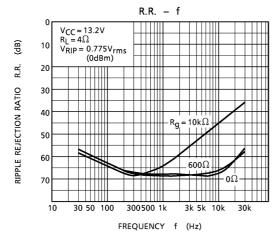


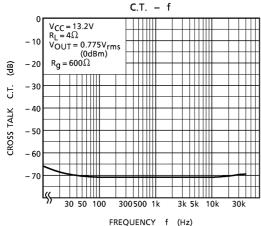


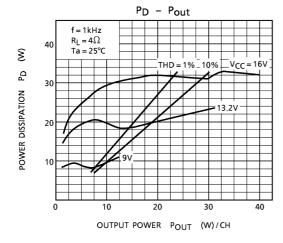


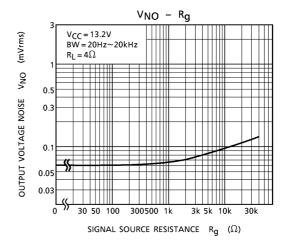


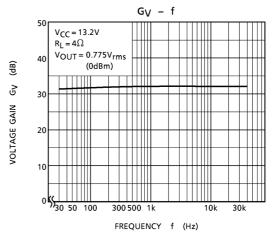


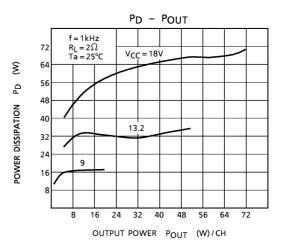


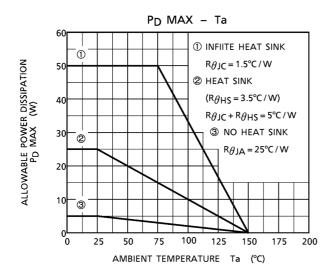






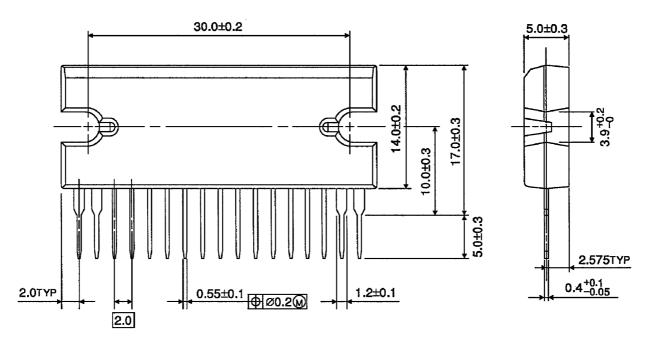




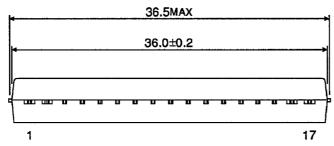


## **Package Dimensions**





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Weight: 9.8g (typ.)

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