TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TA7279P,TA7279AP**

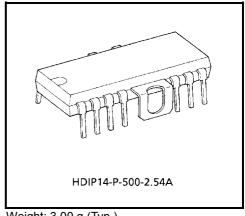
#### **DUAL BRIDGE DRIVER**

The TA7279P, TA7279AP are dual bridge driver designed for DC motor rotation control.

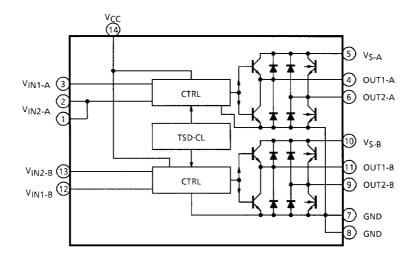
#### **FEATURES**

- Wide Range of Operating Voltage
  - :  $V_{CC \text{ (opr.)}} = 6 \sim 18 \text{ V (P, AP)},$  $V_{S \text{ (opr.)}} = 0 \sim 16 \text{ V (P)} / = 0 \sim 18 \text{ V (AP)}$
- Output Current Up to 1.0 A (AVE.), 3.0 A (PEAK)
- Built-in Thermal Shut Down and Current Limiter
- Input Hysteresis for Stable Operation

#### **BLOCK DIAGRAM**



Weight: 3.00 g (Typ.)



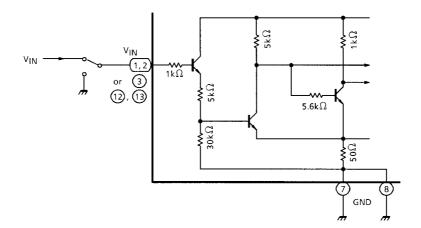
## **PIN FUNCTION**

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION		
1	V <sub>IN2</sub> -A	A-ch input terminal		
2	V <sub>IN2</sub> -A	A-cit input terminal		
3	V <sub>I N1-A</sub>	A-ch input terminal		
4	OUT1-A	A-ch output terminal		
5	V <sub>S</sub> - <sub>A</sub>	A-ch Motor drive power supply		
6	OUT2-A	A-ch output terminal		
7	GND	GND terminal		
8	GND	GND terminal		
9	OUT2-B	B-ch output terminal		
10	V <sub>S-B</sub>	B-ch Moter drive power supply		
11	OUT1-B	B-ch output terminal		
12	V <sub>IN1-B</sub>	B-ch input terminal		
13	V <sub>IN2-B</sub>	B-ch input terminal		
14	V <sub>CC</sub>	Logic power supply		

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### **APPLICATION NOTE**

## (1) Input circuit



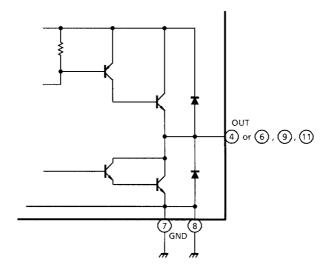
Input terminals of (2), (3), (12) and (13) Pin are all high active type and have a hysteresis.

 $3~\mu A$  Typ. of input current is required.

The input circuit is an active high type, as shown in the diagram. When voltage higher than the specified  $V_{\rm IN}$  (H) is applied, the output is logic "H". When voltage lower than the specified

 $V_{IN}$  (L) is applied or if the input is grounded, the output is logic "L". Since the input current IN flows to the input when logic "H", be careful with the output impedance at the previous step.

### (2) Output circuit



## **FUNCTION**

IN1	IN2	OUT1	OUT2	MODE
1	1	L	L	Brake
0	1	L	CW / CCW	
1	0	H L CCW/CV		CCW / CW
0	0	High Im	STOP	

# **MAXIMUM RATINGS (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Supply Voltage	AP	V <sub>CC</sub> (MAX.)	25	V	
Supply Vollage	Р	VCC (IVIAX.)	20		
Motor Drive Voltage	AP	V <sub>S</sub> (MAX.)	25	V	
Wolor Drive Voltage	Р	VS (IVIAA.)	18		
Output Current	PEAK	I <sub>O</sub> (PEAK)	3.0	Α	
Output Current	AVE.	I <sub>O</sub> (AVE.)	1.0		
Power Dissipation		P <sub>D</sub> (Note)	2.3	W	
Operating Temperature		T <sub>opr</sub>	-30~75	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C	

Note: No heat sink.

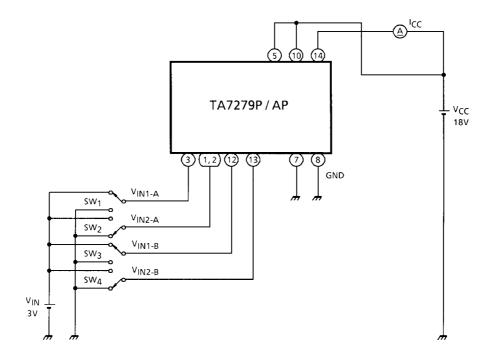
# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current		I <sub>CC1</sub>	1	V <sub>CC</sub> = 18 V, Output Off, Stop mode	14	28	41	mA
		I <sub>CC2</sub>	1	V <sub>CC</sub> = 18 V, Output Off, CW / CCW mode	10	29	38	
		I <sub>CC3</sub>	1	V <sub>CC</sub> = 18 V, Output Off, Brake mode	8	20	35	
Input Operating Voltage	1 (High)	V <sub>IN (H)</sub>	_	T <sub>j</sub> = 25°C	3.0	_	V <sub>CC</sub>	V
	2 (Low)	V <sub>IN (L)</sub>	_	T <sub>j</sub> = 25°C		_	0.8	
Input Current		I <sub>IN</sub>	2	Sink, V <sub>IN</sub> = 3 V	-	3	10	μA
Output Saturation Voltage	Upper	V <sub>SATU-1</sub>	3	I <sub>O</sub> = 0.1 A, V <sub>CC</sub> = V <sub>S</sub> = 18 V	_	_	1.1	V
	Lower	V <sub>SATL-1</sub>	3	I <sub>O</sub> = 0.1 A, V <sub>CC</sub> = V <sub>S</sub> = 18 V	_	_	1.0	
	Upper	V <sub>SATU-2</sub>	3	I <sub>O</sub> = 1.0 A, V <sub>CC</sub> = V <sub>S</sub> = 18 V	_	1.2	1.5	
	Lower	V <sub>SATL-2</sub>	3	I <sub>O</sub> = 1.0 A, V <sub>CC</sub> = V <sub>S</sub> = 18 V	_	1.05	1.4	
Leakage Current	Upper	I <sub>LU</sub>	_	V <sub>S</sub> = 25 V	_	_	50	μA
	Lower	I <sub>LL</sub>	_	V <sub>S</sub> = 25 V	_	_	50	
Diode Forward Drop	Upper	V <sub>FU</sub>	4	I <sub>F</sub> = 1 A	_	2.0	_	- V
	Lower	V <sub>FL</sub>	4	I <sub>F</sub> = 1 A	_	1.3	_	

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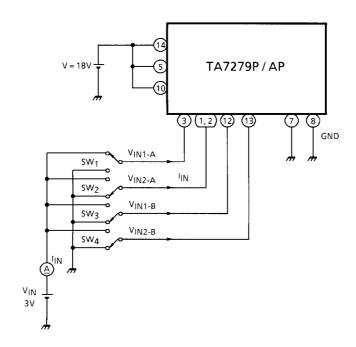
## **TEST CIRCUIT 1.**

ICC1, 2, 3



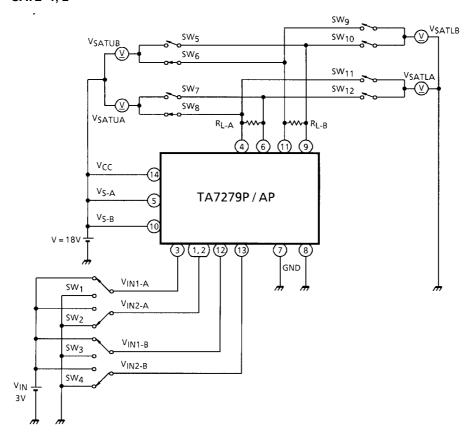
## **TEST CIRCUIT 2.**

IIN (H), (L)

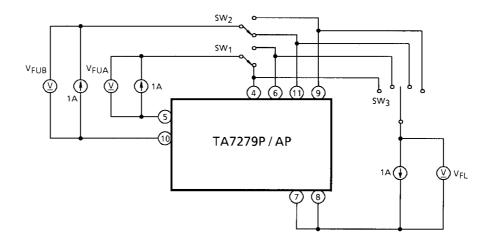


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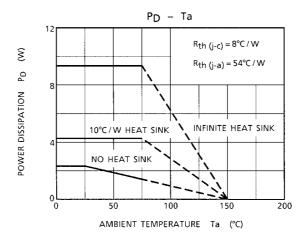
# TEST CIRCUIT 3. VSATU-1, 2 / VSATL-1, 2



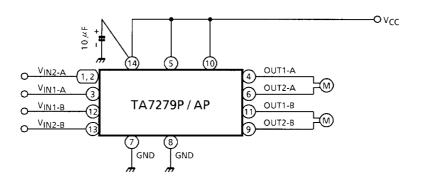
# TEST CIRCUIT 4. $V_{FU, L}$



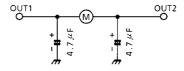
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### **APPLICATION CIRCUIT**



Problems may result if a capacitor is inserted in parallel to the motor as a measure against noise. If measures against noise are necessary, connect capacitors as shown in the diagram below. A larger bypass capacitor between  $V_{CC}$  and GND is effective against noise and other problems. (A capacitance higher than  $100~\mu F$  is recommended.)



Note: Utmost care is necessary in the design of the output line, V<sub>S</sub> and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

## **PACKAGE DIMENSIONS**

HDIP14-P-500-2.54A Unit: mm 11.0±0.2 15.6±0.2 12.7 27.0MAX 26.5±0.2 6.25±0.2 3.2±0.2 2.8±0.3 0.5±0.1 ⊕ 0.25 M

Weight: 3.00 g (Typ.)

1.82TYP

2.54

5.08

1.2±0.1

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