

# TA2055F

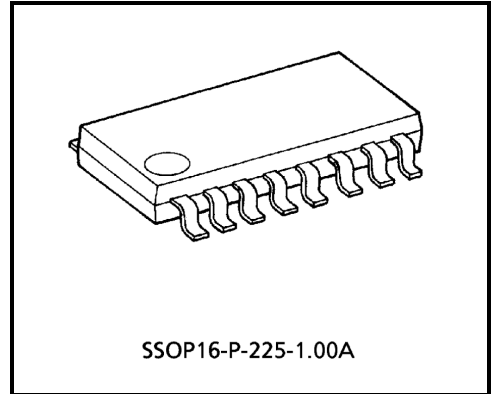
## Filter IC For $\Sigma$ - $\Delta$ Modulation System DA Converter

TA2055F is an analog filter IC for  $\Sigma$ - $\Delta$  modulation system DA converter.

Using the TA2055F in combination the TC9237BF, TC9270F (the  $\Sigma$ - $\Delta$  modulation system DA converter with a built-in digital filter), it is possible to construct a DA conversion system with less external parts.

### Features

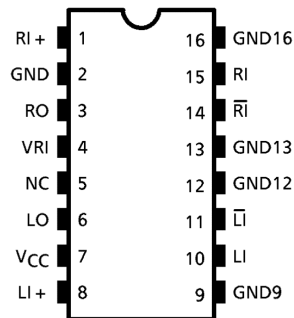
- Built-in CR for LPFs and output (differential) amplifiers for the left and right channel.
- Single power supply operation.
- Noise distortion factor and S / N ratio are as follows (when operating at +5V single power supply):  
 Noise distortion factor: -93dB (typ.)  
 S / N: 100dB (typ.)



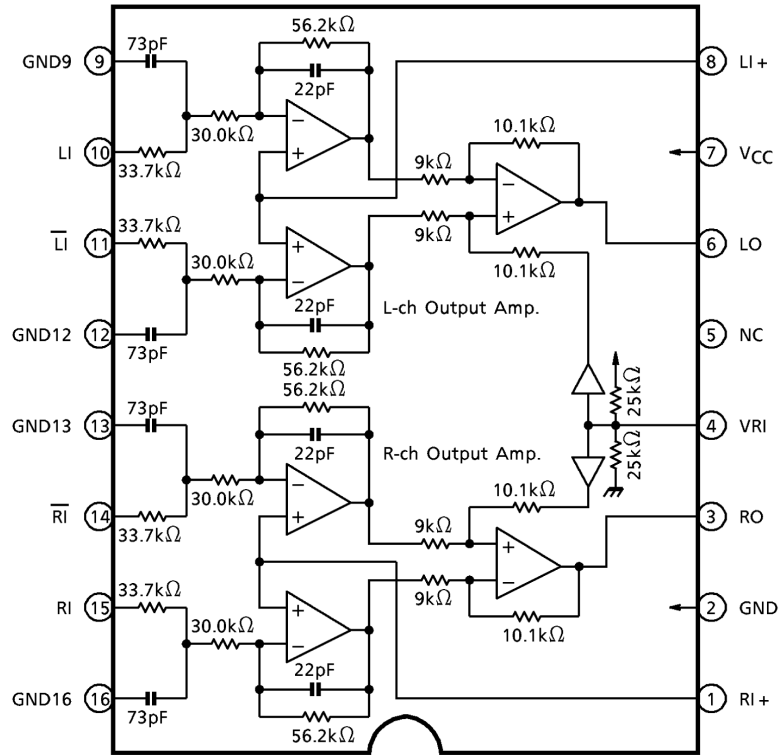
SSOP16-P-225-1.00A

Weight: 0.14g (typ.)

### Pin Connection (top view)



**Block Diagram**



**Description Of Pin Functions**

Pin No.	Symbol	I / O	Function & Operation	Remarks
1	RI +	I	R channel operational amplifier forward input pin. Connect to VRI.	—
2	GND	—	Ground pin.	—
3	RO	O	R channel analog output pin.	—
4	VRI	—	Reference voltage pin. ( $V_{CC} / 2$ )	See the block diagram
5	NC	—	Non-connecting pin. NC pin is used in the open state.	—
6	LO	O	L channel analog output pin.	—
7	V <sub>CC</sub>	—	Supply voltage pin.	—
8	LI +	I	L channel operational amplifier forward input pin. Connect to VRI.	—
9	GND9	—	Ground pin for L channel reverse input side filter.	—
10	LI	I	L channel forward input pin.	Connect to LO of TC9270F
11	LI-bar	I	L channel reverse input pin.	Connect to LO-bar of TC9270F
12	GND12	—	Ground pin for L channel forward input side filter.	—
13	GND13	—	Ground pin for R channel forward input side filter.	—
14	RI-bar	I	R channel reverse input pin.	Connect to RO-bar of TC9270F
15	RI	I	R channel forward input pin.	Connect to RO of TC9270F
16	GND16	—	Ground pin for R channel reverse input side filter.	—

## Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	11	V
Power dissipation	P <sub>D</sub>	350 (*)	mW
Operating temperature	T <sub>opr</sub>	-35~85	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

(\*) Reduce 2.8mW / °C at Ta = above 25°C.

## Electrical Characteristics (unless otherwise specified, V<sub>CC</sub> = 5V, Ta = 25°C)

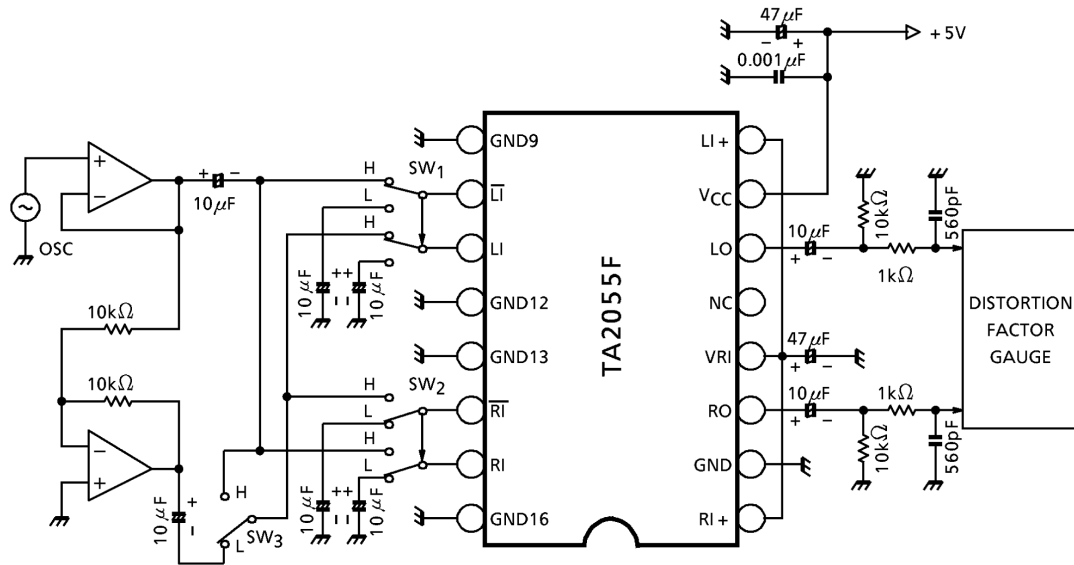
Characteristic	Symbol	Test Circuit	Test Condition	Min.	Typ.	Max.	Unit
Operating supply voltage	V <sub>CC</sub>	—	Ta = -35~85°C	4.5	5.0	10	V
Operating supply current	I <sub>CCQ</sub> (1)	—	At no signal	7.5	10.0	12.5	mA
	I <sub>CCQ</sub> (2)		At no signal, V <sub>CC</sub> = 10V	8.2	11.0	13.8	
Reference voltage	V <sub>RI</sub>	—	—	2.45	2.50	2.55	V
Noise distortion factor	THD (1)	1	1kHz, V <sub>O</sub> = 950mV <sub>rms</sub>	—	-93	-90	dB
	THD (2)		10kHz, V <sub>O</sub> = 950mV <sub>rms</sub>	—	-93	-90	
	THD (3)		1kHz, V <sub>O</sub> = 95mV <sub>rms</sub>	—	-78	-75	
Cross talk	CT	1	1kHz, V <sub>O</sub> = 950mV <sub>rms</sub>	—	-100	-95	dB
Attenuation	ATT (1)	1	40kHz, V <sub>O</sub> = -10dBV <sub>rms</sub>	0.51	0.71	1.41	dB
	ATT (2)		80kHz, V <sub>O</sub> = -10dBV <sub>rms</sub>	1.50	2.70	4.50	
Max. output level	V <sub>omax</sub>	1	1kHz, THD = 1%	1.20	1.25	—	V <sub>rms</sub>
Differential balance	G <sub>VB</sub>	1	1kHz, 1.1dBV <sub>rms</sub> In-phase input	—	—	-40	dB
LR output difference	G <sub>VD</sub>	1	1kHz, 1.1dBV <sub>rms</sub> Differential input	—	0	0.5	dB

(Note 1) When the TC9270F and +5V single power supply are operated  
: Full scale = 950mV<sub>rms</sub> (typ.).

(Note 2) The amount of attenuations is based on 1kHz, V<sub>O</sub> = -10dBV<sub>rms</sub>.

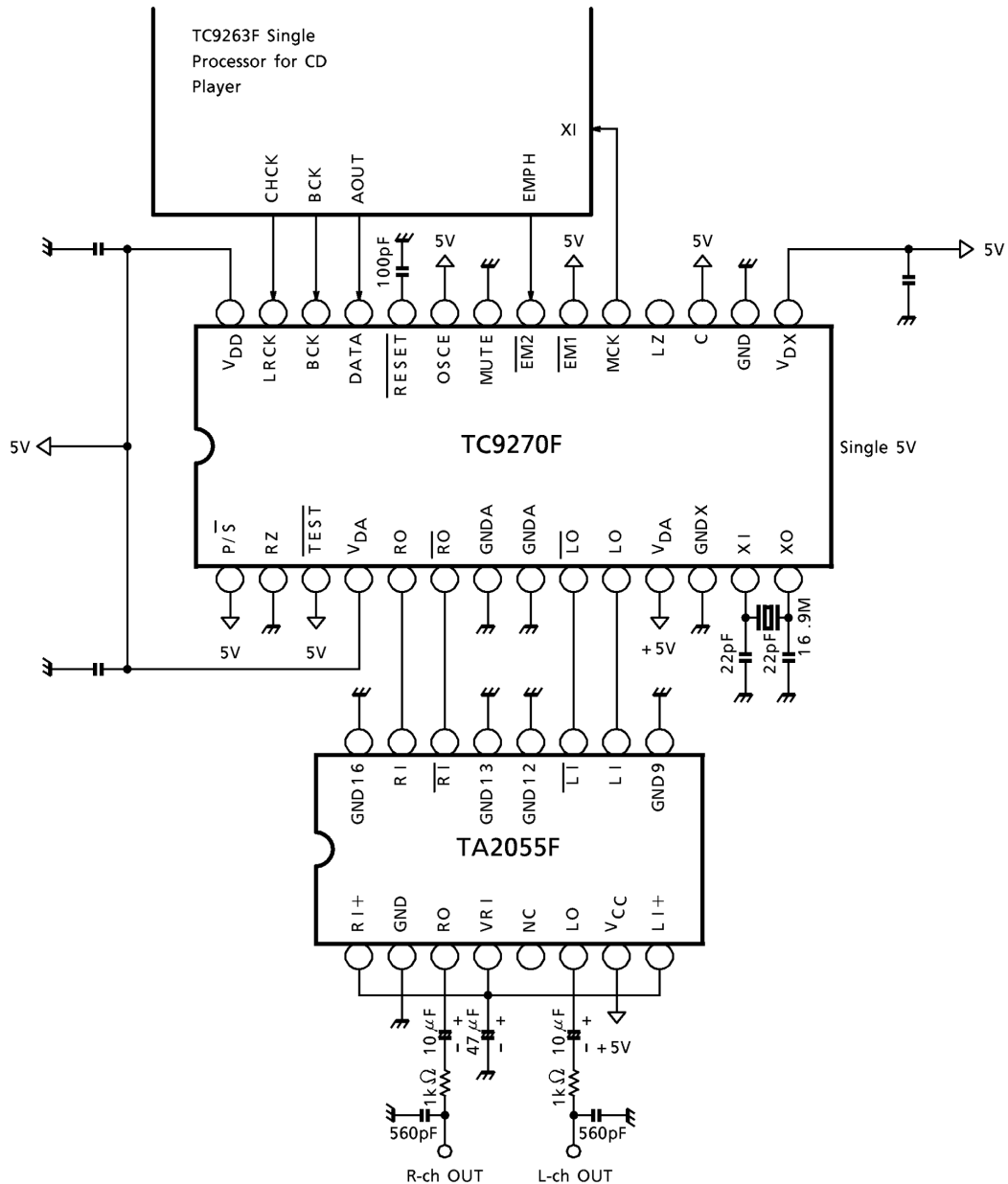
(Note 3) Measuring circuit-1: Indicates the measuring circuit.

**Test Circuit-1**



SW <sub>1</sub>	SW <sub>2</sub>	SW <sub>3</sub>	Measuring Item
L	L	—	Operating supply voltage, reference voltage
L	H	L	Cross talk (R→L)
H	L	L	Cross talk (L→R)
H	H	L	Noise distortion factor, attenuation, maximum output level, LR output difference
H	H	H	Difference balance

## Application Circuit Example



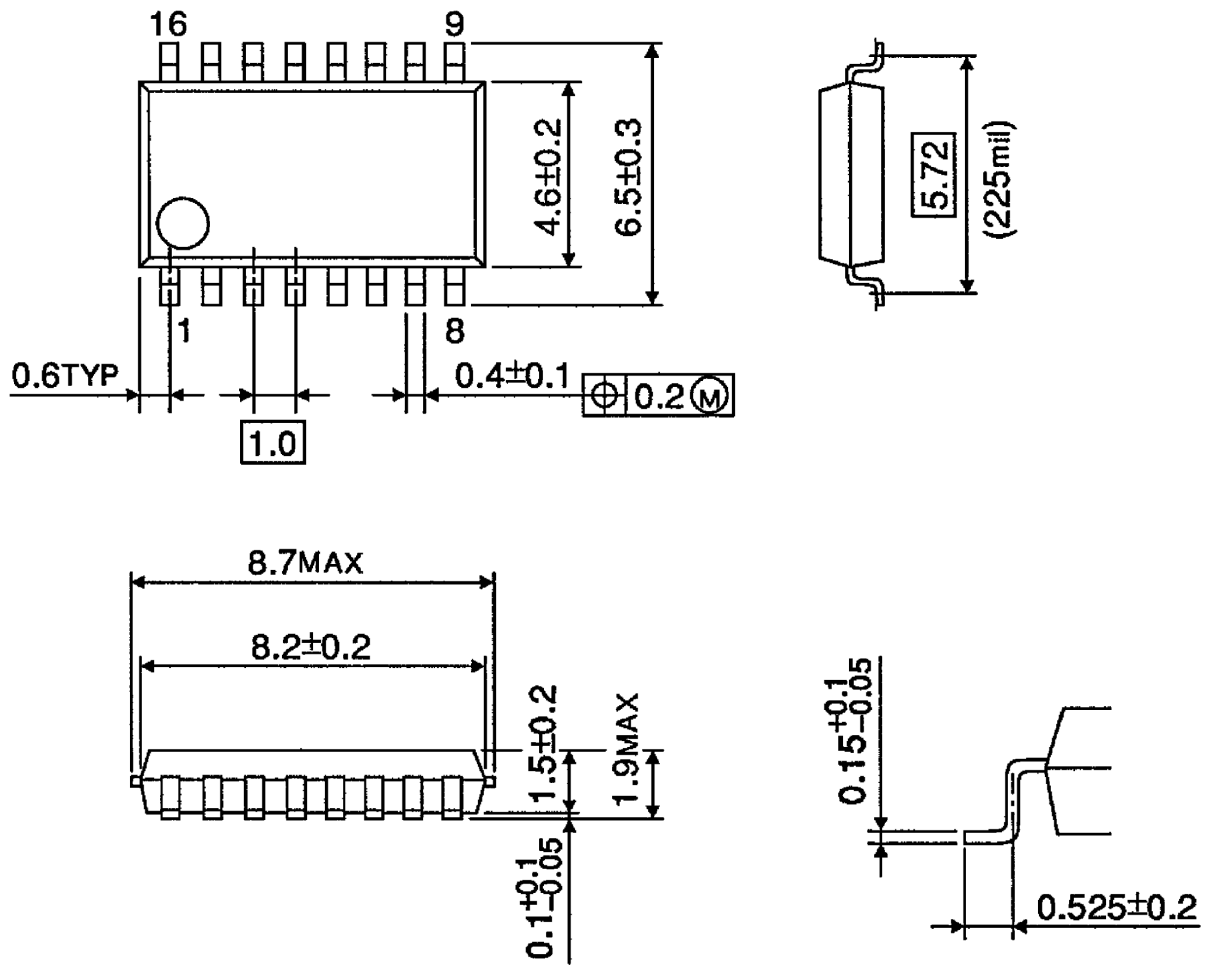
### (Cautions)

- Quality of crystal oscillation waveform largely effects S / N ratio.  
Further, this is also true when system clock is input externally through the XI pin of pin(13).
- Suppress glitch of input signals (LRCK, BCK, DATA) as could as possible.
- The wiring between the TC9270F output and the analog filter amplifier input must be made the shortest
- The capacitor between V<sub>DA</sub> and GNDA shall be connected as close to the pin as possible.
- NC pin is used in the open state.

**Package Dimensions**

SSOP16-P-225-1.00A

Unit : mm



Weight: 0.14g (typ.)

**RESTRICTIONS ON PRODUCT USE**

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