559-090

Q High-Speed CMOS 10-Bit Bus Switches

QS3384
QS32384

2

FEATURES/BENEFITS

- 5Ω switches connect inputs to outputs
- Direct bus connection when switches on
- Zero propagation delay (QS3384)
- Low power CMOS proprietary technology

- QS32384 is 25Ω version for low noise
- Two enables control five bits each
- Zero ground bounce
- Available in 24-pin PDIP, SOIC, HQSOP and QSOP

DESCRIPTION

The QS3384 and QS32384 each provide a set of ten high-speed CMOS TTL-compatible bus switches. The low ON resistance of the QS3384 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The QS32384 adds an internal 25Ω resistor to reduce reflection noise in high-speed applications. The Bus Enable ($\overline{BE}$) signals turn the switches on. Two bus enable signals are provided, one for each of the upper and lower five bits of the two 10-bit buses.
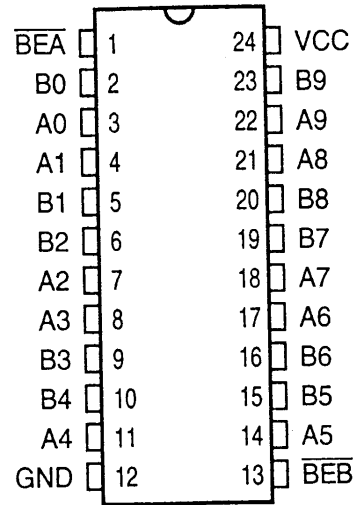
FUNCTIONAL BLOCK DIAGRAM
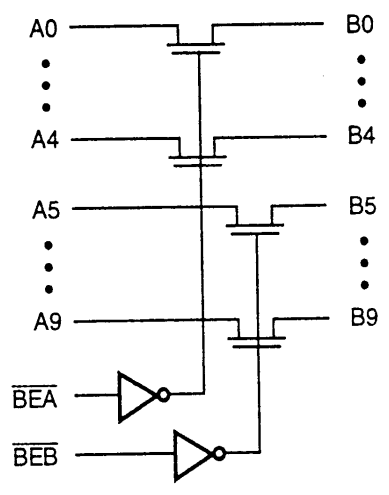
PIN DESCRIPTION

Name	I/O	Function
A9-A0	I/O	Bus A
B9-B0	I/O	Bus B
$\overline{BEA}$, $\overline{BEB}$	I	Bus Switch Enable

PIN CONFIGURATION
(All Pins Top View)

PDIP, SOIC, QSOP, HQSOP

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           _____
$\overline{BEA}$ [  1      24 ] VCC
    B0 [  2      23 ] B9
    A0 [  3      22 ] A9
    A1 [  4      21 ] A8
    B1 [  5      20 ] B8
    B2 [  6      19 ] B7
    A2 [  7      18 ] A7
    A3 [  8      17 ] A6
    B3 [  9      16 ] B6
    B4 [ 10      15 ] B5
    A4 [ 11      14 ] A5
   GND [ 12      13 ] $\overline{BEB}$
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FUNCTION TABLE

$\overline{BEA}$	$\overline{BEB}$	B4-B0	B9-B5	Function
H	H	Hi-Z	Hi-Z	Disconnect
L	H	A4-A0	Hi-Z	Connect
H	L	Hi-Z	A9-A5	Connect
L	L	A4-A0	A9-A5	Connect

ABSOLUTE MAXIMUM RATINGS

Supply Voltage to Ground .... −0.5V to +7.0V
DC Switch Voltage Vs ....−0.5V to +7.0V
DC Input Voltage Vin ....−0.5V to +7.0V
AC Input Voltage (for a pulse width ≤ 20 ns) ....−3.0V
DC Output Current Max. Sink Current/Pin .... 120 mA
Maximum Power Dissipation .... 0.5 watts
Tstg Storage Temperature ....−65° to +150°C

Note: Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to QSI devices that result in functional or reliability type failures.

CAPACITANCE

$T_A = 25°C$, f = 1 MHz, $V_{IN} = 0V$, $V_{OUT} = 0V$

Pins	SOIC		QSOP		PDIP		HQSOP		Unit
	Typ	Max	Typ	Max	Typ	Max	Typ	Max	
Control Pins	3	4	3	4	4	5	6	7	pF
QuickSwitch Channels	7	8	7	8	8	9	10	11	pF

Note: Capacitance is characterized but not tested.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Commercial: T_A = 0°C to 70°C, V_{CC} = 5.0V ± 5% Military: T_A = –55°C to 125°C, V_{CC} = 5.0V ± 10%

Symbol	Parameter	Test Conditions		Min	Typ[1]	Max	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Inputs		2.0	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Inputs		—	—	0.8	V
$\mid I_{IN} \mid$	Input Leakage Current[2]	$0 \leq V_{IN} \leq V_{CC}$		—	—	1	μA
$\mid I_{OZ} \mid$	Off-State Current (Hi-Z)	$0 \leq A, B \leq V_{CC}$		—	.001	1	μA
I_{OS}	Short Circuit Current[3]	A(B) = 0V, B(A) = Vcc		—	300	—	mA
R_{ON}	Switch ON Resistance[4,5]	Vcc = Min., V_{IN} = 0.0V I_{ON} = 30 mA	3384 (Com)	—	5	7	Ω
			3384 (Mil)	—	10	15	
			32384 (Com)	20	28	40	
			32384 (Mil)	—	35	45	
R_{ON}	Switch ON Resistance[4,5]	Vcc = Min., V_{IN} = 2.4V I_{ON} = 15 mA	3384 (Com)	—	10	15	Ω
			3384 (Mil)	—	15	20	
			32384 (Com)	20	35	48	
			32384 (Mil)	—	40	55	

Notes:
1. Typical values indicate Vcc = 5.0V and T_A = 25°C.
2. During input/output leakage, testing all pins are at a HIGH or LOW state, and the $\overline{BE}$ control is HIGH.
3. Not more than one output should be used to test this high power condition and the duration is ≤1 second.
4. Measured by voltage drop between A and B pins at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.
5. Max. value R_{ON} guaranteed but not tested.

Typical ON Resistance vs V_{IN} at 4.75 Vcc (QS3384 Only)
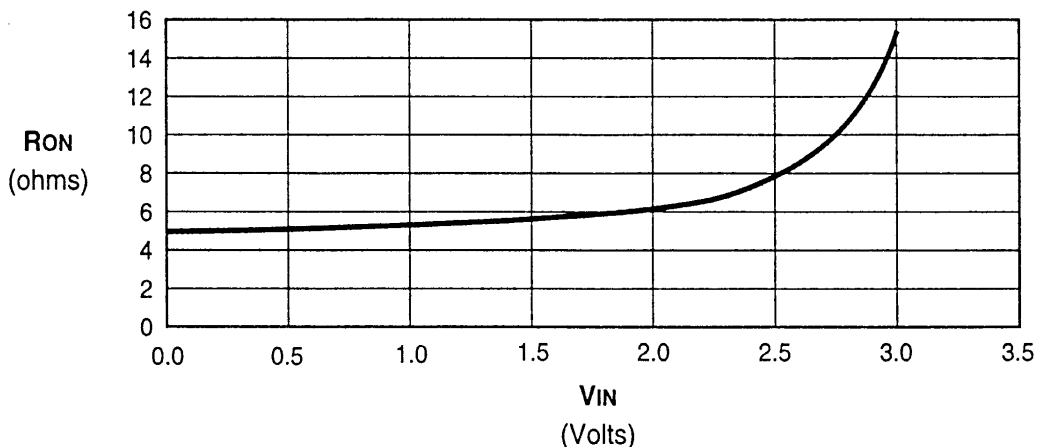
POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Max	Unit
I_{CCQ}	Quiescent Power Supply Current	Vcc = Max., V_{IN} = GND or Vcc, f = 0	1.5	mA
ΔI_{CC}	Power Supply Current per Input HIGH[2]	Vcc = Max., V_{IN} = 3.4V, f = 0 per Control Input	2.5	mA
Q_{CCD}	Dynamic Power Supply Current per MHz[3]	Vcc = Max., A and B Pins Open, Control Inputs Toggling @ 50% Duty Cycle	0.25	mA/MHz

Notes:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC specifications.
2. Per TTL driven input (V_{IN} = 3.4V, control inputs only). A and B pins do not contribute to Icc.
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed by design, but not tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

Commercial: TA = 0°C to 70°C, Vcc = 5.0V ± 5%   Military: TA = –55°C to 125°C, Vcc = 5.0V ± 10%
C_{LOAD} = 50 pF, R_{LOAD} = 500Ω unless otherwise noted.

Symbol	Description[1]		QS3384			QS32384			Unit
			Min	Typ	Max	Min	Typ	Max	
t_{PLH} t_{PHL}	Data Propagation Delay[2,4] Ai to Bi, Bi to Ai	COM MIL	— —	— —	0.25[3] 0.75[3]	— —	— —	1.25[3] 1.75[3]	ns
t_{PZL} t_{PZH}	Switch Turn-on Delay[1] $\overline{BEA}$, $\overline{BEB}$ to Ai, Bi	COM MIL	1.5 1.5	— —	6.5 7.5	1.5 1.5	— —	7.5 8.5	ns
t_{PLZ} t_{PHZ}	Switch Turn-off Delay[1,2] $\overline{BEA}$, $\overline{BEB}$ to Ai, Bi	COM MIL	1.5 1.5	— —	5.5 6.5	— —	— —	5.5 6.5	ns
$|Q_{CI}|$	Charge Injection[5,6]	COM MIL	— —	1.5 1.5	— —	— —	1.5 1.5	— —	pC

Notes:
1. See Test Circuit and Waveforms. Minimums guaranteed but not tested.
2. This parameter is guaranteed by design but not tested.
3. The time constant for the switch alone is of the order of 0.25 ns for 50 pF.
4. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
5. Measured at switch turn off, A to C, load = 50 pF in parallel with 10 meg scope probe, V_{IN} at A = 0.0V.
6. Characterized parameter but not production tested.