



FLIP-FLOP TRUTH TABLE

Inputs				Output
D	C	R	S	Q _n
0	0	0	0	Q _{n-1}
0	0	0	1	1
0	0	1	0	0
0	0	1	1	*
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	*
1	0	0	0	Q _{n-1}
1	0	0	1	1
1	0	1	0	0
1	0	1	1	*
1	1	0	0	1
1	1	0	1	1
1	1	1	0	0
1	1	1	1	*

*Output State Undefined

V_{CC1} = 1
 V_{CC2} = 16
 V_{EE} = 8

Total Power Dissipation = 750 mW typ/pkg
 Shift Frequency = 325 MHz typ

4-Bit Shift Register

The MC1694 is a 4-bit register capable of shift rates up to 325 MHz (typical). This shift register operates in the shift-right mode, accepting serial data at either data input D1 or D2. A master reset and individual set inputs override the clock allowing asynchronous entry of information.

MC1694

SHIFT REGISTERS