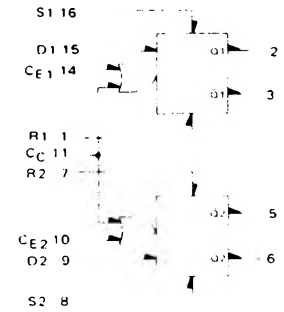

TRUTH TABLE

Inputs					Outputs	
R	S	D	C_E	C_C	Q	$\bar{Q}$
L	H	◇	◇	◇	H	L
H	L	◇	◇	◇	L	H
L	L	H	⌐	⌐	H	L
L	L	H	L	⌐	H	L
L	L	H	⌐	L	H	L
L	L	H	L	L	Q	$\bar{Q}$
L	L	L	⌐	⌐	L	H
L	L	L	L	⌐	L	H
L	L	L	⌐	L	L	H
L	L	L	L	L	Q	$\bar{Q}$

R,S cannot be High at the same time
◇ Don't Care
⌐ Low to High Transition

V_{CC} = Pin 4
V_{EE1} = Pin 12
V_{EE2} = Pin 13

P_D = 525 mW typ/pkg. (No Load)
f_{tog} = 500 MHz typ.

Dual Type D Master-Slave Flip-Flop

The MC1605 is a high speed dual master-slave type D flip-flop. It is designed for use in high bandwidth systems. Each flip-flop has its own clock input in addition to a common clock enable input. Each flip-flop has a Q and $\bar{Q}$ output. The outputs may not be wire-ORed.

Data is transferred to the output with the rising edge of the clock. Each flip-flop has its own set and reset input, and they override the clock input.