MC1355

BALANCED MONOLITHIC FOUR-STAGE HIGH-GAIN FM/IF AMPLIFIER

 \dots designed for use with Foster-Seeley discriminator or ratio detector in high quality FM systems.

- High AM Rejection (60 dB typ)
- Wide Range of Supply Voltages (8 to 18 Vdc)
- Low Distortion (0.5% typ)

LIMITING FM IF AMPLIFIER

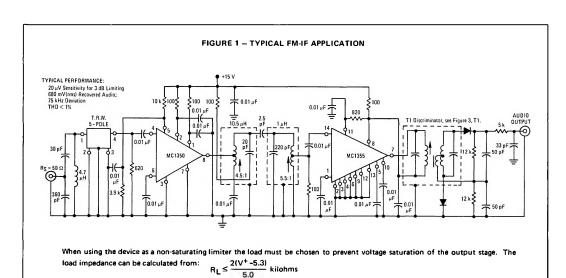
MONOLITHIC SILICON INTEGRATED CIRCUIT



PSUFFIX PLASTIC PACKAGE CASE 605 TO-116



PQ SUFFIX PLASTIC PACKAGE CASE 647



See Packaging Information Section for outline dimensions.

MC1355 (continued)

MAXIMUM RATINGS ($T_A = +25^{\circ}C$ unless otherwise noted)

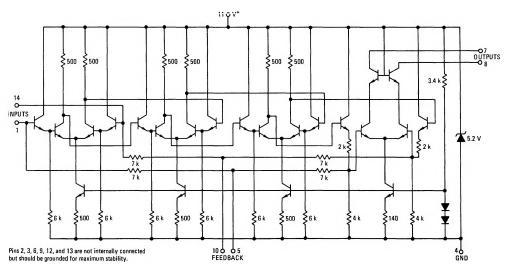
Rating	Value	Unit Vdc	
Output Voltage (pins 7 & 8)	40		
Supply Current to pin 11	20	20 mA	
Input Signal Voltage (single-ended)	5.0	Vp-p	
Input Signal Voltage (differential)	10	Vp-p	
Power Dissipation (package limitation) Derate above T _A = +25 ⁰ C	625 5.0		
Operating Temperature Range (Ambient)	0 to +75	°c	
Storage Temperature Range	-65 to +150	°c	

Maximum Ratings as defined in MIL-S-19500, Appendix A.

ELECTRICAL CHARACTERISTICS (V $^+$ = 15 Vdc, f = 10.7 MHz, TA = +25°C, RS = 820 ohms unless otherwise noted)

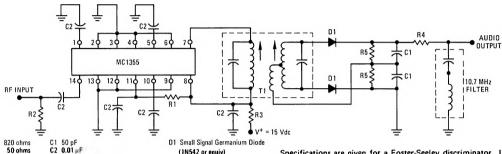
Characteristic		Min	Тур	Max	Units
Power Supply Voltage Range		8.0	15	18	Vdc
Total Circuit Current		_	16	_	mAdc
Total Output Stage Current			4.2	-	mA
Device Dissipation			125	_	mW
Internal Zener Voltage			5.2	_	Vdc
Input Signal for 3 dB Limiting		-	175	250	μV(rms)
Output Current Swing		3.5	4.2	5.0	mA p-p
AM Rejection (10 mv to 1.0 v (rms) input, FM @ 100%, AM @ 80%, Foster Seeley detector)		-	60	-	dB
Maximum AM Signal before Breakup (FM @ 100%, AM @ 80%)		-	_	1.4	V(rms)
Admittance Parameters	Y ₁₁ Y ₁₂ Y ₂₁ Y ₂₂	- - - -	120 + j320 j0.6 8 + j5.9 15 + j230	- - -	μmhos μmho mhos μmhos

FIGURE 2 - CIRCUIT SCHEMATIC



TYPICAL CHARACTERISTICS

FIGURE 3 - TEST CIRCUIT



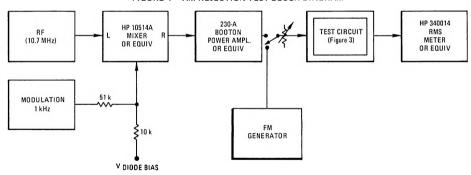
- R1 820 ohms R2 **50 ohms**
- R3 100 ohms R4 5 kilohms R5 12 kilohms
 - - T1 10.7 MHz Foster-Seeley Discriminator, Primary Impedance = 3.9 k, Peak-to-Peak Separation = 600 kHz

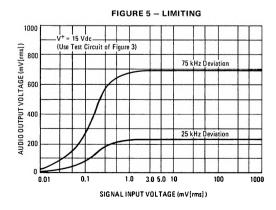
(1N542 or equiv)

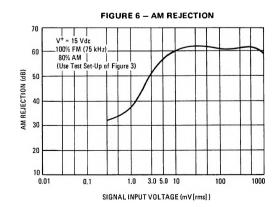
Specifications are given for a Foster-Seeley discriminator. Improved AM rejection at low signal levels can be obtained with a ratio detector.

For optimum circuit stability it is important to ground pins 2, 3, 4, 6, 9, 12, and 13.

FIGURE 4 - AM REJECTION TEST BLOCK DIAGRAM







TYPICAL CHARACTERISTICS (continued)

