

### PRELIMINARY DATA

#### RHYTHM GENERATORS

- 16 PROGRAMMABLE RHYTHMS (CODED FOR THE M258; ALSO AVAILABLE IN COMBINATION FOR THE M259
- 16 OUTPUTS (2 SECTIONS BY 8)
- MASK PROGRAMMABLE RESET COUNTS (24 or 32)
- DOWN BEAT OUT
- SYNC OUT
- EXTERNAL RESET
- TWO CHIP SELECTS (CS1, CS2) FOR SEPARATE TRISTATE CONDITION OF THE TWO OUT-PUT SECTIONS
- INTERNAL PULL-UP ON THE INPUTS
- OPEN DRAIN OUTPUTS WITH RETURN TO "1" STATUS
- CHOICE BETWEEN RETURN TO "1" OR NOT ON 8 OUTPUTS (OUT 1, 2, 3, 4, 9, 10, 11, 12)
   SEPARATELY
- ONLY ONE POWER SUPPLY (+5V)
- VERY LOW POWER CONSUMPTION (150 mW TYP.)

The M258, M259 are monolithic rhythm generators specifically designed for electronic organs and other musical instruments.

Constructed on a single chip using MOS N-channel silicon gate technology, they are supplied in a 28 lead for (M258) or 40 lead for (M259) dual in-line plastic package.

### ABSOLUTE MAXIMUM RATINGS\*

V <sub>DD</sub> **	Source supply voltage	-0.3 to	+7	V
V <sub>i</sub> **	Input voltage	-0.3 to	+7	V
l <sub>o</sub>	Output current (at any pin)		3	mΑ
VoH	Output voltage		12	V
$T_{stg}$	Storage temperature range	-65 to +	125	°C
Top	Operating temperature range	0 to	50	°C

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ORDERING NUMBERS: M258 B1/EB1 for dual in-line plastic package

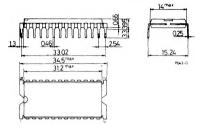
M259 B1/EB1 for dual in-line plastic package

<sup>\*\*</sup> All voltages are with respect to V<sub>SS</sub> (GND).

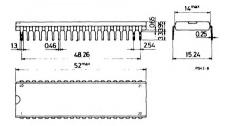


### MECHANICAL DATA (dimensions in mm)

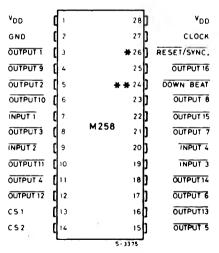
### Dual in-line plastic package (28 lead)



### Dual in-line plastic package (40 lead)



### PIN CONNECTIONS

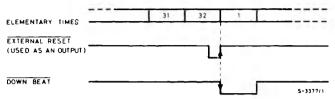


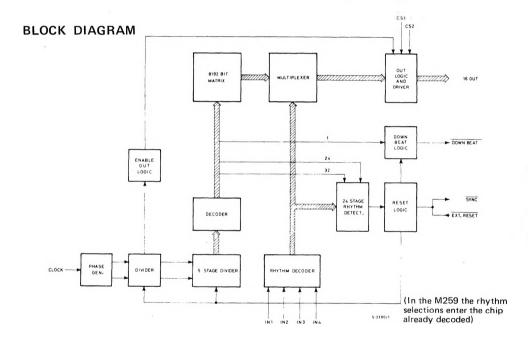
CS1 enables the outputs 01 to 08 CS2 enables the outputs 09 to 16

C	1		40	þ	v <sub>DO</sub>
(	2		39	þ	CLOCK
[	3		38	þ	INPUT 16
ĺ	4		37	þ	INPUT 15
t	5		<b>#</b> 36	þ	RESET/SYNC.
[	6		35	þ	OUTPUT 16
d	7		<b># #</b> 34	b	DOWN BEAT
d	8		33	þ	OUT PUT 8
d	9		32	þ	OUTPUT 15
d	10	M 25	9 31	b	OUTPUT 7
d	11		30	þ	INPUT 14
þ	12		29	þ	INPUT 13
þ	13		28	þ	INPUT 12
þ	14		27	þ	INPUT 11
þ	15		26	þ	OUTPUT14
þ	16		25	þ	INPUT 10
đ	17		24	þ	OUTPUT 6
þ	18		23	þ	INPUT 9
þ	19		22	þ	OUTPUT 13
þ	20		21	þ	OUTPUT 5
١			5-3376	,	
		2	C 2 C 3 C 4 C 5 C 6 C 7 C 8 C 9 C 10 M 25:  C 11 C 12 C 13 C 14 C 15 C 16 C 17 C 18 C 19	[ 2 39 38 4 4 37 4 4 37 5 4 36 6 35 7 4 4 34 32 6 10 M 259 31 11 30 12 29 13 15 15 26 16 25 17 24 19 19 22	[ 2 39] [ 3 38] [ 4 37] [ 5 #36] [ 6 35] [ 7 ##34] [ 8 33] [ 9 32] [ 10 M259 31] [ 11 30] [ 12 29] [ 13 28] [ 14 27] [ 15 26] [ 16 25] [ 17 24] [ 18 23] [ 19 22] [ 19 22]

- \* This is a bidirectional pin. Used as an input it allows the chip reset; used as an output it can reset other devices.
- \*\* This pin generates a down beat trigger which can be used to drive an external lamp to indicate the first beat of the first bar of each rhythm.

### RESET AND DOWN BEAT TIMING WAVEFORMS (POSITIVE LOGIC)





# RHYTHM, SELECTION (for M258 only)

Rhythm	īN4	ĪN3	ĪN2	ĪN1
1	1	1	1	1
2	1	1 1	1	0
3	1	1 1	Ó	1 1
4	1	1	Ō	Ó
5	1 1	0	1	l i
6	1	0	1	Ó
7	1	0	Ó	l ĭ
8	1	0	O	0
9	0	1	1	l i
10	) o	1	1	Ò
11	0	1	0	i
12	0	1	0	0
13	0	0	] 1	j j
14	0	0	1	0
15	0	. 0	0	1
16	0	0	0	0



# **STATIC ELECTRICAL CHARACTERISTICS**(positive logic, $V_{DD} = 4.75$ to 5.25V, $T_{amb} = 0$ to 50°C unless otherwise specified)

	Daramatar			Values		
100	Parameter	Test conditions	Min. Typ	Тур.	Max.	Unit
CLOCI	K INPUT					
V <sub>IH</sub>	Clock high voltage		2.4		V <sub>DD</sub>	٧
VIL	Clock low voltage		0		0.4	V

### **DATA INPUTS**

V <sub>IH</sub>	Input high voltage			2.4		VDD	٧
VIL	Input low voltage			0		0.4	٧
R <sub>IN</sub>	Internal resistance to V <sub>DD</sub>	V1 = 0V	V <sub>DD</sub> = 5V	100	180		ΚΩ
I <sub>OL</sub> (*)	Input load current	Vi=VIL			-50		μА

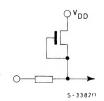
### **EXT. RESET**

V <sub>IH</sub>	Input high voltage			4.5		V <sub>DD</sub>	٧
V <sub>IL</sub>	Input low voltage			0		1.5	٧
R <sub>OFF</sub>	Internal resistance to V <sub>DD</sub> (inactive sync)	V <sub>O</sub> = 0	V <sub>DD</sub> = 5V	100	180		ΚΩ
Ron	Internal resistance to V <sub>DD</sub> (active sync)	V <sub>O</sub> = 1V	V <sub>DD</sub> = 4.75V		260	300	Ω

## **OUTPUTS** (O<sub>i</sub>, Down beat)

R <sub>ON</sub>	V <sub>O</sub> = 1V	260	300	Ω
VoL	Source current = 1 mA	0.26	0.3	V
I <sub>LO</sub>	V <sub>O</sub> = 12V T <sub>amb</sub> = 25°C		10	μΑ
POWER DISSIPATION				
I Supply current	T <sub>amb</sub> = 25°C	30		mΑ

<sup>(\*)</sup> The "High Level" is clamped by the internal pull-up.





Values

Typ.

Max.

Min.

Unit

**DYNAMIC ELECTRICAL CHARACTERISTICS** (positive logic,  $V_{DD} = 4.75$  to 5.25V,  $T_{amb} = 0$  to 50°C unless otherwise specified)

Test condistions

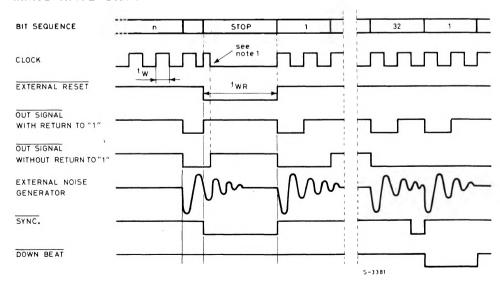
f	Clock repetition rate		DC	100	KHz
t <sub>w</sub>	Pulse width	Measured at 50% of the swing	5		μs
tr	Rise time	Measured between 10% and 90% of the swing		100	μs
tf	Fall time	Measured between 10% and 90% of the swing		100	μs

### **EXT. RESET**

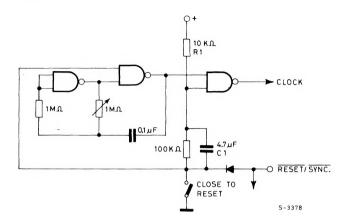
twR	Pulse width	100		μs
tCR	Clock delay with respect to reset	0		μs

### TIMING WAVEFORMS

Parameter

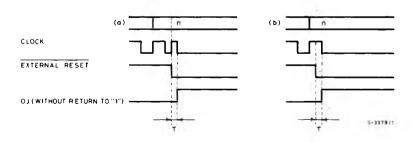


Note 1: This additional pulse, to reset the outputs without return to "1", can be obtained by using a clock generator as shown in the following diagram:



Ext. Reset/Sync. is a bidirectional pin. Used as an input it can reset the circuit as shown in the timing diagram and used as an output it can drive the reset of other devices.

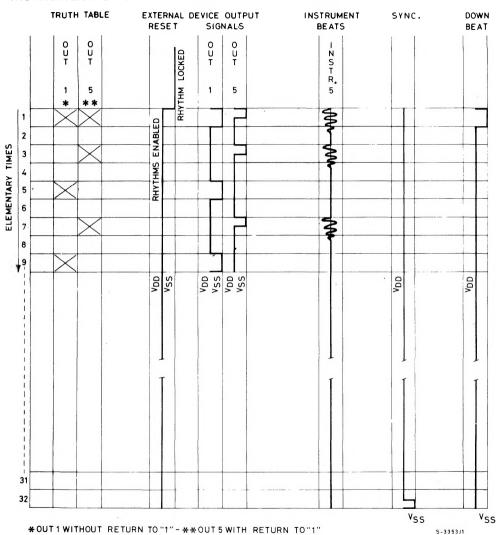
Using the clock generator shown in the above figure, when the switch is closed asynchronous with respect to the clock, it is possible to have to two cases (see the following diagrams); in both the cases the output reset can be obtained by CS1 and CS2.



In both the cases the delay  $\tau$  (in the outputs without return to "1") is defined through the constant R1 C1  $\geqslant$  10  $\mu$ sec.



### INSTRUMENT BEATS VERSUS RHYTHM PROGRAM



Note: The outputs 01 to 08 are enabled by CS1; the outputs 09 to 16 are enabled by CS2. The outputs 01 to 04 and 09 to 12 are programmable separately without return to "1".