Reversible motor driver

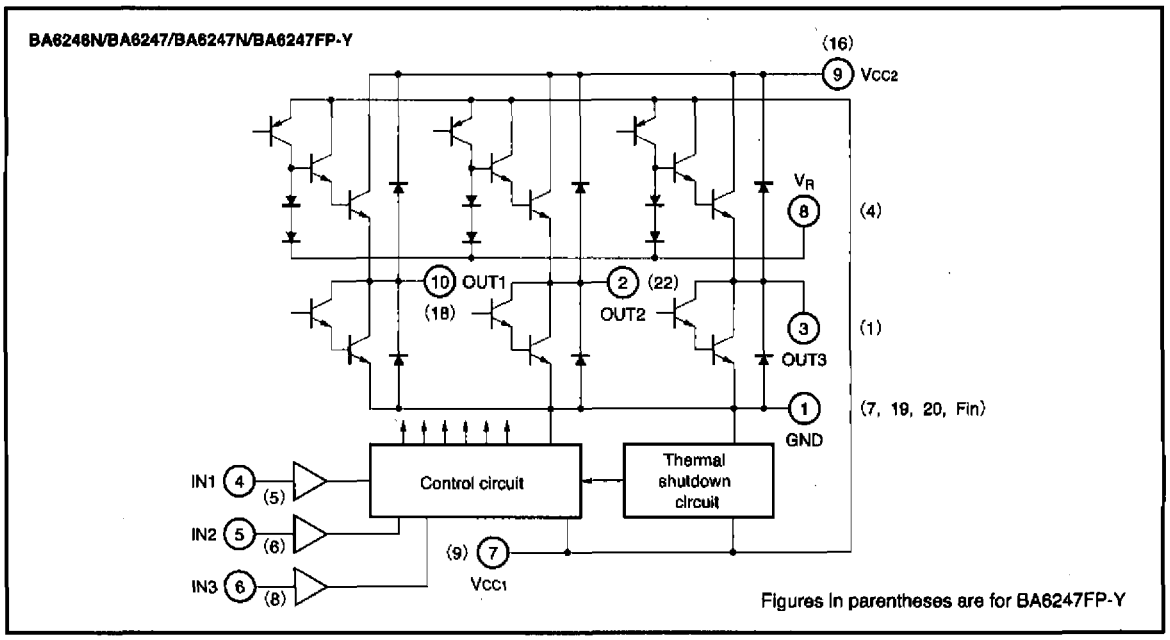
BA6246N/BA6247/BA6247N/BA6247FP-Y

The BA6246N, BA6247, BA6247N, and BA6247FP-Y are monolithic ICs incorporating two reversible-motor drivers. The ICs differ in the control logic and output mode.

●Features

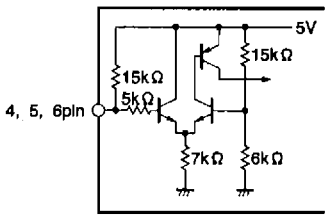
1) Two reversible-motor drivers in each unit.
2) Built-in thermal shutdown circuit.
3) Output voltage can be set arbitrarily.
4) Available in a compact SIP10pin package (BA6246N, BA6247N) or a HSIP10pin package with radiation fins (BA6247).
5) Available in a HSOP25pin surface-mount package (BA6247FP-Y).

●Block diagram

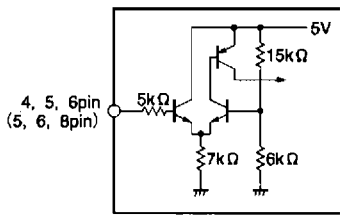


●Control pin equivalent circuits

BA6246N



BA6247 / BA6247N / BA6427FP - Y



Figures in parentheses are for BA6247FP-Y

Fig.1

●Input/output truth table

Input			Output					
Common to all units			BA6246N			BA6247/BA6247N/BA6247FP-Y		
IN1	IN2	IN3	OUT1	OUT2	OUT3	OUT1	OUT2	OUT3
L	L	L / H	L	L	L	L	L	L
H	L	L	H	L	OPEN	H	L	OPEN
H	L	H	L	H	OPEN	L	H	OPEN
L	H	L	H	OPEN	L	H	OPEN	L
L	H	H	L	OPEN	H	L	OPEN	H
H	H	L / H	OPEN	OPEN	OPEN	L	L	L

●Absolute maximum ratings (Ta=25℃)

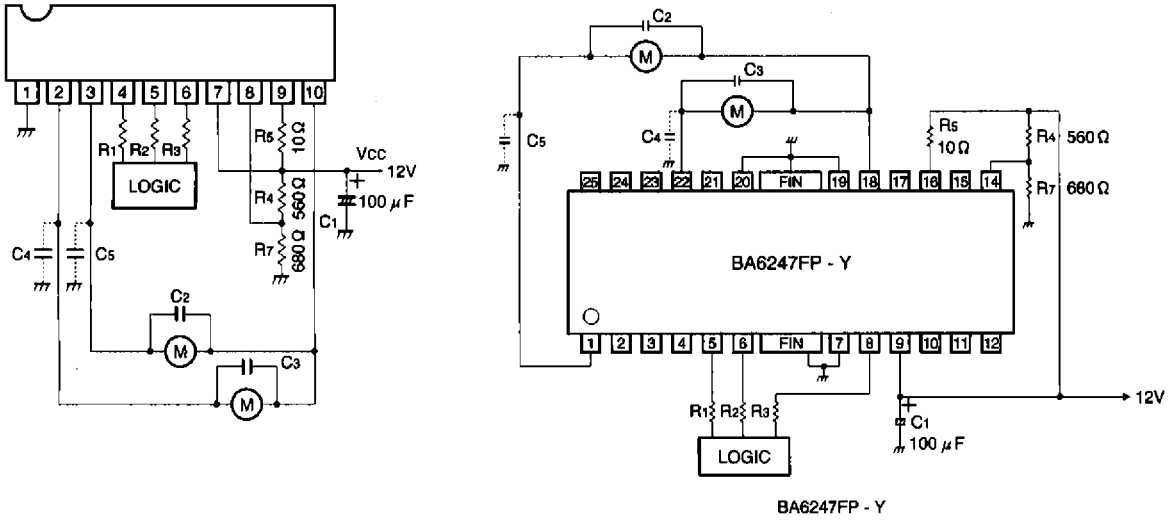
Parameter	Symbol	Limits			Unit
		BA6246N BA6247N	BA6247	BA6247FP - Y	
Power supply voltage	Vcc	20	20	20	V
Power dissipation	Pd	1000*1	2000*3	1450*4	mW
Operating temperature	Topr	−25~75	−25~75	−25~75	℃
Storage temperature	Tstg	−55~125	−55~150	−55~150	℃
Input voltage	VIN	−0.2~6	−0.2~6	−0.2~6	V
Output current	Io	1.0*2	1.0*2	1.0*2	A

*1 Reduce power by 10 mW for each degree above 25 ℃.
*2 50 ms pulse with a duty ratio of 1/50
*3 Reduce power by 16.0 mW for each degree above 25 ℃.
*4 When a glass epoxy PCB (90 X 50 X 1.6 mm) is used; reduce power by 11.6 mW for each degree above 25 ℃.

●Electrical characteristics (Unless otherwise noted, Ta=25℃ and V_{CC}=12V)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Conditions
Operating supply voltage		V_{CC1}, V_{CC2}	8	—	18	V	—
Output reference voltage		V_R	—	—	18	V	—
Current consumption	BA6246N	I_{CC}	—	7	15	mA	IN1=IN2=IN3= "L"
	BA6247 / BA6247N / BA6247FP - Y		—	10	20		
LOW level input voltage		V_{IL}	—	—	1.0	V	
HIGH level input voltage		V_{IH}	3.5	—	—	V	
LOW level output voltage		V_{OL}	—	0.9	1.5	V	V_R : OPEN, I_O=0.5A
HIGH level output voltage		V_{OH}	10.0	10.5	—	V	V_R : OPEN, I_O=0.5A
Output offset voltage		V_{OIS}	−0.5	0	0.5	V	V_R=6V, I_O=0.5A Difference between V_R and the output voltage
Pin 8 (pin 14) source current		I_8 (I_{14})	0.5	0.8	1.6	mA	V_R=6V, I_O=0.5A

●Application example



BA6247FP - Y

C2, C3: Capacitors for preventing parasitic oscillation. Though the optimum capacitance depends on such PCB arrangement factors as the power supply circuit, motor characteristics, and conductor foil patterns, a range of 0.01-1 μF is recommended.

C4, C5: Capacitors for preventing parasitic oscillation. They may or may not be required, depending on the PCB arrangement. A capacitance range of 0.01-10 μF is recommended.

Fig.2 Application circuits for a motor with ratings of 6 V and 100 mA (V_{CC} = 12 V)

●Operation notes

(1) Input conditions

1) The input threshold voltage is positively correlated with temperature as expressed by :

$$\frac{\Delta V_{IH}}{\Delta T} \fallingdotseq +2.8mV/℃$$

$$\frac{\Delta V_{IH}}{\Delta T} \fallingdotseq +1.6mV/℃ \text{ (Typ.)}$$

2) The input pins of the BA6246N are pulled up through a resistance of about 15kΩ (see Fig. 1). To secure the LOW level input, the interface to these pins should have a current-sink capability of at least 700 μA (5V/15kΩ×2).

3) The maximum input voltage is 6V. Make sure that the input will not exceed this value.

(2) Changes in motor direction

When reversing the rotational direction of a motor, make sure to go through the brake or open mode in-between the opposite directions.

The duration of brake mode should be more than the braking time, which is defined by the time required for the potential of the LOW level output pin to become less than the ground potential by the electromotive force generated when the mode is switched from rotation to brake.

The duration of open mode should be 1 ms or more.

(3) Due to the effects of capacitors C_2~C_5, the motor that is not being driven could be momentarily driven during mode switching. Check for this problem when designing your application.

(4) It is recommendable to arrange your design so that voltage rises at V_{CC1} prior to V_{CC2} when turning on the power, and voltage falls at V_{CC1} after V_{CC2} when turning off the power.

(5) Thermal shutdown circuit

When the thermal shutdown circuit is activated, the outputs are left OPEN. The circuit is activated when the IC junction temperature rises above 170℃. The temperature difference between the activation and deactivation settings is about 30℃.

●Power dissipation curves

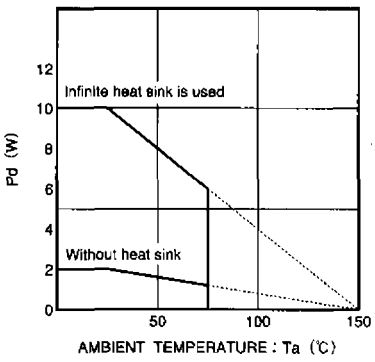


Fig.3 Power dissipation curves (BA6247)

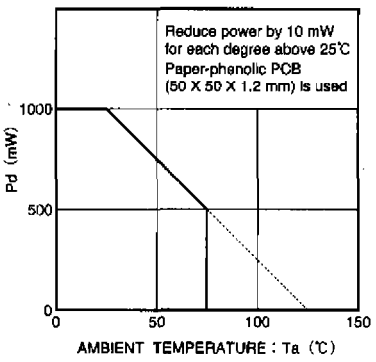


Fig.4 Power dissipation curve (BA6246N, BA6247N)

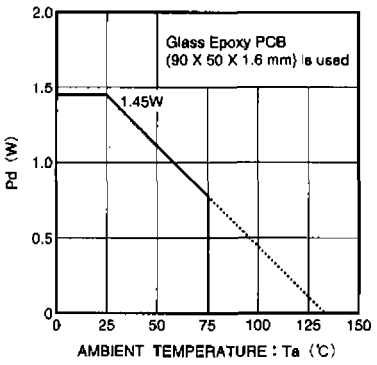


Fig.5 Power dissipation curve (BA6247FP-Y)

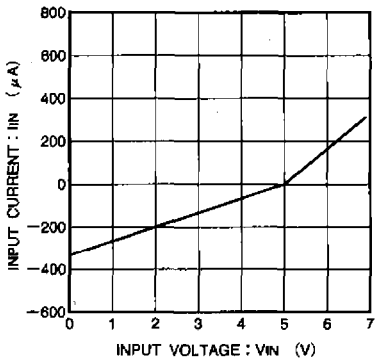


Fig.6 Input voltage vs. input current (BA6246N)

●Electrical characteristic curves

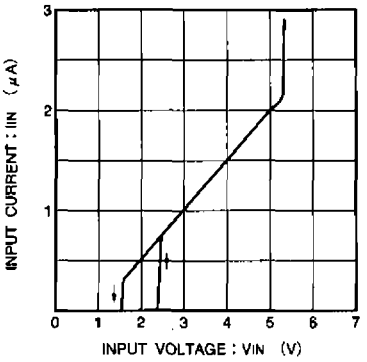


Fig.7 Input voltage vs. input current (BA6247,BA6247N,BA6247FP-Y)

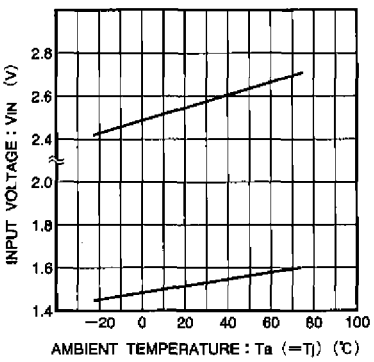


Fig.8 Input voltage vs. ambient temperature

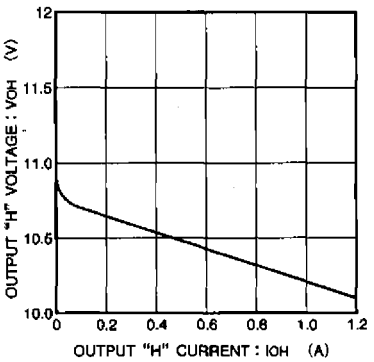


Fig.9 HIGH level output voltage vs. output current

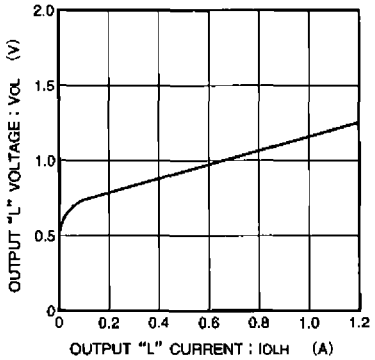


Fig.10 LOW level output voltage vs. output current

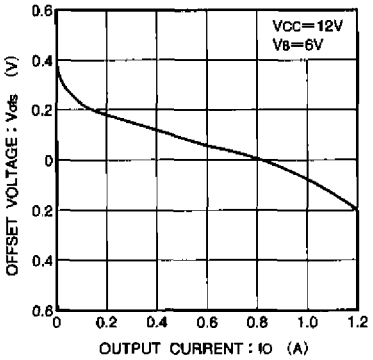


Fig.11 Offset voltage vs. output current

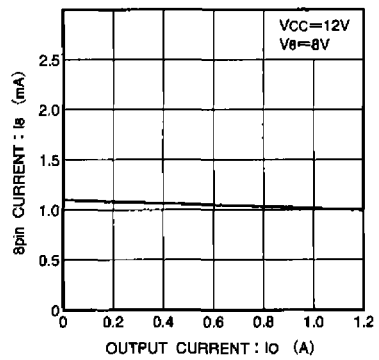
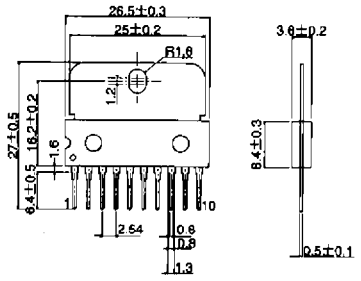


Fig.12 Pin-6 current vs. output current

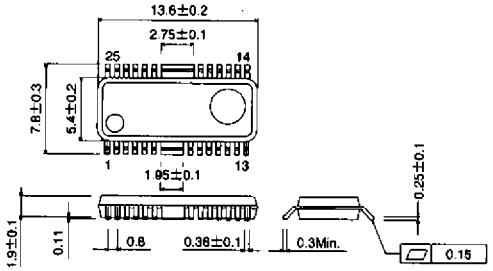
●External dimensions (Units: mm)

BA6247



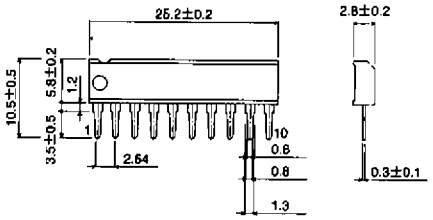
HSIP10

BA6247FP - Y



HSOP25

BA6246N/BA6247N



SIP10

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