

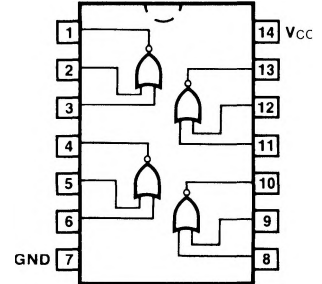
54LS/74LS28

QUAD 2-INPUT NOR BUFFER

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V} \pm 5\%$, $T_A = 0^\circ \text{C to } +70^\circ \text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%$, $T_A = -55^\circ \text{C to } +125^\circ \text{C}$	
Plastic DIP (P)	A	74LS28PC		9A
Ceramic DIP (D)	A	74LS28DC	54LS28DM	6A
Flatpak (F)	A	74LS28FC	54LS28FM	3I

CONNECTION DIAGRAM
PINOUT A



INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74LS (U.L.) HIGH/LOW
Inputs	0.5/0.25
Outputs	30/15 (7.5)

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER	54/74LS		UNITS	CONDITIONS	
		Min	Max		$V_{IN} = \text{Gnd}$	$V_{CC} = \text{Max}$
I_{CCH}	Power Supply Current		3.6	mA	$V_{IN} = \text{Gnd}$	$V_{CC} = \text{Max}$
I_{CCL}			13.8		$V_{IN} = \text{Open}$	
t_{PLH} t_{PHL}	Propagation Delay		20 20	ns	Figs. 3-1, 3-4	

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ \text{C}$ and $V_{CC} = +5.0 \text{ V}$.