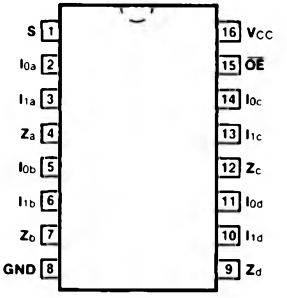
54LS/74LS257A

QUAD 2-INPUT MULTIPLEXER

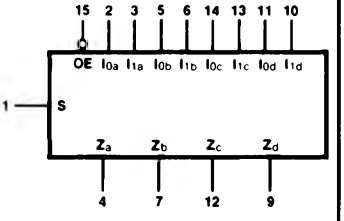
(With 3-State Outputs)

CONNECTION DIAGRAM
PINOUT A



DESCRIPTION — The '257A is the same as the '257, except that the output drive capability is increased as indicated in the tables below. The ac test limits are the same as the '257 but with the test load changed to 667 Ω and 45 pF, except for the Output Disable Time tests, whose load is 667 Ω and 5 pF. For all other information please refer to the '257 data sheet.

LOGIC SYMBOL



VCC = Pin 16
Gnd = Pin 8

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE VCC = +5.0 V ±5%, TA = 0° C to +70° C	MILITARY GRADE VCC = +5.0 V ±10%, TA = -55° C to +125° C	PKG TYPE
Plastic DIP (P)	A	74LS257APC		9B
Ceramic DIP (D)	A	74LS257ADC	54LS257ADM	6B
Flatpak (F)	A	74LS257AFC	54LS257AFM	4L

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	54/74LS (U.L.) HIGH/LOW
Z_n	3-State Outputs	65/15 (25)/(7.5)

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER		54/74LS Min	54/74LS Max	UNITS	CONDITIONS	
VOL	Output LOW Voltage	XM, XC		0.4	V	IOL = 12 mA	VCC = Min
		XC		0.5		IOL = 24 mA	
IOS	Output Short Circuit Current		-30	-130	mA	VCC = Max	