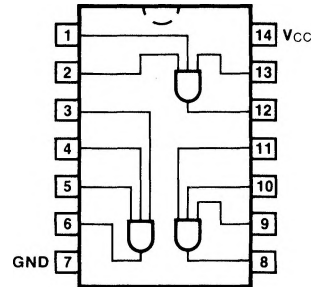


54S/74S15
54LS/74LS15

TRIPLE 3-INPUT AND GATE
(With Open-Collector Outputs)

CONNECTION DIAGRAM
PINOUT A



ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0\text{ V} \pm 5\%$, $T_A = 0^\circ\text{C to } +70^\circ\text{C}$	$V_{CC} = +5.0\text{ V} \pm 10\%$, $T_A = -55^\circ\text{C to } +125^\circ\text{C}$	
Plastic DIP (P)	A	74S15PC, 74LS15PC		9A
Ceramic DIP (D)	A	74S15DC, 74LS15DC	54S15DM, 54LS15DM	6A
Flatpak (F)	A	74S15FC, 74LS15FC	54S15FM, 54LS15FM	3I

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74S (U.L.) HIGH/LOW	54/74LS (U.L.) HIGH/LOW
Inputs	1.25/1.25	0.5/0.25
Outputs	OC**/12.5	OC**/5.0 (2.5)

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER	54/74S		54/74LS		UNITS	CONDITIONS	
		Min	Max	Min	Max		$V_{IN} = \text{Open}$	$V_{CC} = \text{Max}$
I_{CCH}	Power Supply		19.5	3.6		mA		
I_{CCL}	Current		42	6.6			$V_{IN} = \text{Gnd}$	
t_{PLH} t_{PHL}	Propagation Delay		8.5 9.0	20 15		ns	Figs. 3-2, 3-5	

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ\text{C}$ and $V_{CC} = +5.0\text{ V}$.

**OC—Open Collector