

# 54FCT/74FCT827A • 54FCT/74FCT827B

## 10-Bit Buffer/Line Driver with TRI-STATE® Outputs

### General Description

The 'FCT827A/B 10-bit bus buffer provides high performance bus interface buffering for wide data/address paths or buses carrying parity. The 10-bit buffers have NOR output enables for maximum control flexibility.

FACT™ FCTA/B utilizes NSC quiet series technology to provide improved quiet output switching and dynamic threshold performance.

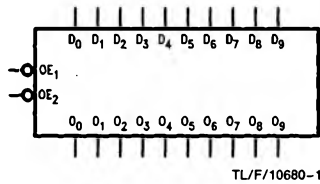
FACT FCTA features GTO™ output control and undershoot corrector in addition to a split ground bus for superior performance.

FACT FCTB features an undershoot corrector in addition to a split ground bus for superior performance.

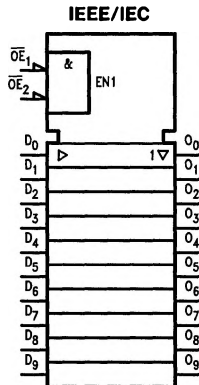
### Features

- NSC 54FCT/74FCT827A/B is pin and functionally equivalent to IDT 54FCT/74FCT827A/B
- High Speed parallel registers with positive edge-triggered D-type flip-flops
- Input clamp diodes to limit bus reflections
- TTL/CMOS input and output level compatible
- $I_{OL} = 48 \text{ mA (Com)}, 32 \text{ mA (Mil)}$
- CMOS power levels
- 4 kV minimum ESD immunity
- Military Product compliant to MIL-STD 883

### Logic Symbols

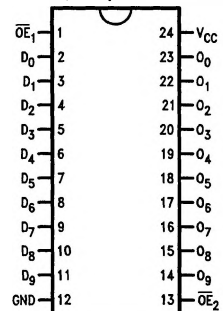


Pin Names	Description
$\overline{OE}_1, \overline{OE}_2$	Output Enable
$D_0 - D_7$	Data Inputs
$O_0 - O_7$	Data Outputs



### Connection Diagrams

Pin Assignment for DIP, Flatpak and SOIC



Pin Assignment for LCC

