



# 54ACTQ/74ACTQ843

## Quiet Series 8-Bit Transparent Latch

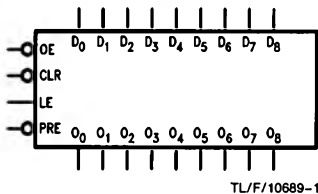
### General Description

The 'ACTQ843 bus interface latch is designed to eliminate the extra packages required to buffer existing latches and provide extra data width for wider address/data paths. The 'ACTQ843 utilizes NSC Quiet Series technology to guarantee quiet output switching and improved dynamic threshold performance. FACT Quiet Series™ features GTOTM output control and undershoot corrector in addition to a split ground bus for superior performance.

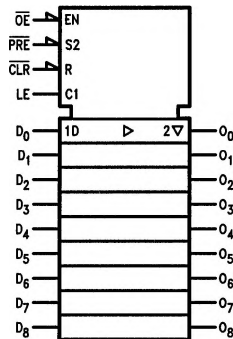
### Features

- Guaranteed simultaneous switching noise level and dynamic threshold performance
- Guaranteed pin-to-pin skew AC performance
- Inputs and outputs on opposite sides of package for easy interface with microprocessors
- Improved latch-up immunity
- Outputs source/sink 24 mA
- 'ACTQ843 has TTL-compatible inputs
- Functionally and pin-compatible to AMD's AM29843
- 4 kV minimum ESD immunity
- 'ACT843 has TTL-compatible inputs
- TRI-STATE® outputs for bus interfacing

### Logic Symbols



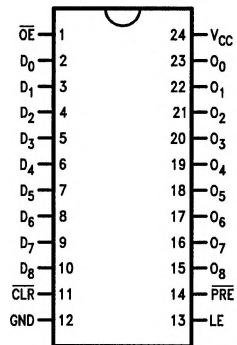
TL/F/10689-1



TL/F/10689-4

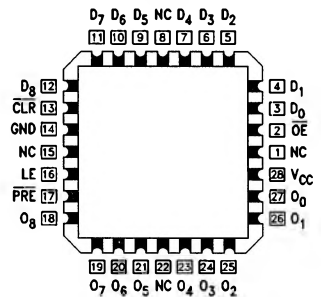
### Connection Diagrams

#### Pin Assignment for DIP, Flatpak and SOIC



TL/F/10689-2

#### Pin Assignment for LCC



TL/F/10689-3

Pin Names	Description
D <sub>0</sub> -D <sub>7</sub>	Data Inputs
O <sub>0</sub> -O <sub>7</sub>	Data Outputs
OE	Output Enable
LE	Latch Enable
CLR	Clear
PRE	Preset