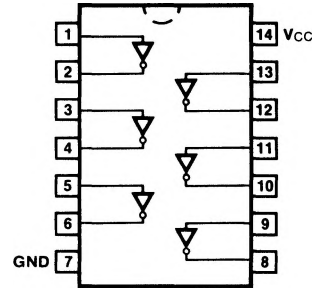


54/7406**HEX INVERTER BUFFER/DRIVER**
(With Open-Collector High-Voltage Output)**CONNECTION DIAGRAM**
PINOUT A

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V} \pm 5\%$, $T_A = 0^\circ \text{C to } +70^\circ \text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%$, $T_A = -55^\circ \text{C to } +125^\circ \text{C}$	
Plastic DIP (P)	A	7406PC		9A
Ceramic DIP (D)	A	7406DC	5406DM	6A
Flatpak (F)	A	7406FC	5406FM	3I

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74 (U.L.) HIGH/LOW
Inputs	1.0/1.0
Outputs	OC**/10

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER	54/74		UNITS	CONDITIONS	
		Min	Max			
V _{OL}	Output LOW Voltage	XC	0.7	V	I _{OL} = 40 mA	V _{CC} = Min V _{IN} = V _{IH}
		XM	0.7		I _{OL} = 30 mA	
		XC, XM	0.4		I _{OL} = 16 mA	
I _{OH}	Output HIGH Current		0.25	mA	V _{OH} = 30 V, V _{CC} = Min V _{IN} = V _{IL}	
I _{CCH}	Power Supply Current		48	mA	V _{IN} = Gnd	V _{CC} = Max
I _{CCL}			51		V _{IN} = Open	
t _{PLH}	Propagation Delay		15	ns	Fig. 3-2, 3-4	
t _{PHL}			23			

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ \text{C}$ and $V_{CC} = +5.0 \text{ V}$.

**OC—Open Collector